Developing Experimental setup for Trapped Ion based Optical Atomic Clock

Thesis submitted to AcSIR For the Award of the Degree of DOCTOR OF PHILOSOPHY In Physical Sciences



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Abstract

Among the seven basic units of measurement in International System (SI) of units, "second" is measured most accurately with the help of atomic clocks. The evolution of science and technology demands the updation of the precision in measurements to improve the time and frequency metrology. Being the National Metrology Institute (NMI) of India at CSIR-NPL, we are designing a state-of-the-art optical atomic clock using a single trapped Ytterbium (Yb) ion, which will have more accuracy compare to the present microwave based atomic clock. In future, the optical atomic clock is expected to be the new definition of the SI second and peruser time keeping with an inaccuracy over the age of the universe (13.8 billion years). Developing the Yb ion based optical clock involves the accomplishment of a plethora of subsegments including designing, fabrication, instrumentation, various measurements, in the areas of vacuum system, optical setups, electronic modules, software development and so on. This thesis expounds the instrumentation part of the precision ion-trap experiment which includes indigenously designed and developed devices which are requisites of the experiment. In addition, theoretical study is carried out on estimation of the BBR shift, magic wavelength and tuneout wavelength, for in-depth understanding of the systematics. During the measurement, the clock states of Yb ion get perturbed due to environmental factors like black body radiation, AC and DC electric and magnetic fields, leading to uncertainty in the measured quantity. The correct knowledge of the perturbation can be accounted to minimize the uncertainty. Due to this reason we calculate the static and dynamic polarizability of the clock states. We also calculate the magic and tune out wavelengths which may be used as a tool for trapping ions in an optical lattice leading to suppression of their micromotions. Probing this experiment for clock transition follows steps like production of ions, trapping them in an ion trap, laser cooling and state preparation, exciting the clock transition and detection etc. Controlling all the associated modules manually within a small interval of time is not possible and thus requires a complete automation so that their operation is free from human errors. To solve this problem, we have developed a universal digitally operated addressing cum control module, the details of which are described in this thesis. During fabrication of the precision ion trap, there will be some machining errors which results to spatial shifting of the trap center relative to the center of the vacuum chamber. To correct this we use compensating electrodes which connect with a redundant and stable low noise DC voltage source to control the position of ion in the trap. Our indigenously developed

voltage source are presented in this thesis, that is able to give output from 0 to 100 V DC with a resolution of 3 mV and long term stability of 1.9 mV. The voltage source is completely computer controlled and programmable. For cooling of the ions, there is requirement of correct laser frequency matching to the resonance transition. To stabilize the laser frequency with respect to any reference point, we designed and developed field programmable gate array based low noise servo controller which contain function generator, lock-in amplifier and Proportional-Integrator-Derivative controller. Using this module we are able to lock frequency of our laser for a week. In case of Yb ion, reference points required for locking some of its transition frequencies is unavailable to be driven by the conventional saturation absorption spectroscopy technique. Due to this reason, we use transfer cavity technique where a laser is frequency stabilized with respect to an atomic transition and is then used to stabilize a Fabry Perot (FP) cavity. This stabilized FP cavity is then treated as the reference cavity to lock frequencies of other useful lasers using standard Pound-Drever Hall technique. Frequency of laser light is often required to be shifted by hundreds of MHz to few GHz for which generally AOMs and EOMs are used. In this thesis, the design and development of the in-house built AOM and EOM drivers has also been reported. Both the drivers are fully computer controlled and automated. Upon development of the described instrumentation, at present we are in a situation to trap ytterbium ion towards our aimed experiment. The work pursued within the scope of my doctoral study, we developed several important instrumentation for CSIR-NPL's mission mode project "Indigenous development of the state-of-the-art quantum standard of time" particulary for its one of the objective "Optical frequency standard".

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Chapter 1

Introduction

Measurement of any physical quantity is important from the viewpoint of establishing a clear understanding of a particular phenomena. For measuring various physical quantities, there are seven basic International system of units (SI units) [1]. Each of the SI units are referenced with respect to constants such as, for mass and temperature has units kilogram and Kelvin that are referenced to the Planck and Boltzmann constants, respectively [1]. Among these seven SI units, "second" is one of the most important one because it is not only used for measuring time or frequency but it also defines references for other SI units [1]. Due to this reason, defining reference for time & frequency standard is crucial for scientific measurement and advanced technology. For maintaining time & frequency standard, atomic clocks are used where oscillator frequency is measured with respect to an atomic transition frequency. In case of Caesium atomic clocks, transition frequency (9.192631770 GHz) between ground state hyperfine levels of 133 Cs is used as a reference frequency that defines one second in SI unit [1]. The Cs-based microwave clock provides a stable frequency with a fractional uncertainty of 10^{-16} [2]. So far, time can be measured most accurately among all other SI units. The evolution of science and technology demands more accurate clock for different applications such as, global navigation satellite system, radio telescope based on very long baseline interferometry, probing fundamental science, communication system, smart power grids, quantum communication, cyber securities, etc. [3]. Performance of a clock is defined by its "stability" and "accuracy". For that Allan deviation is a measure to characterize clock's performance. The Allan deviation of an atomic clock is defined by the equation [4]

$$\sigma(\tau) = \frac{\Delta\nu}{\nu_{\rm o}} \times \sqrt{\frac{T_c}{N\tau}},\tag{1.1}$$

where $\Delta \nu$ is measured linewidth of the atomic transition, ν_{\circ} is the transition frequency, N is number of species probed in a single measurement, T_c is time required for a single measurement and τ is the measurement period. The stability of atomic clock thus becomes better with an increased quality factor $\nu_0/\Delta\nu$ of the oscillator and a better signal to noise ratio $\sqrt{\frac{T_c}{N\tau}}$. Of



these two parameters, increment in quality factor is achieved by using an optical frequency at few hundreds of THz rather than microwave frequency (GHz). The natural linewidths are narrow or ultra-narrow due to choice of forbidden or highly forbidden transitions. Sometimes, $\Delta \nu$ is limited by linewidth of the probe lasers than the linewidth of the transition. This leads to a 1000 times improvement in the quality factor. This has motivated us towards developing an optical atomic clock to improve the time and frequency metrology in India and stay at par to the international standard. At present, the best existing optical atomic clock gives a stable reference frequency with a fractional uncertainty of 10^{-18} [5] which is 100 times better than the microwave laser cooled Cesium based clocks. The evolution of optical clocks begun after the invention of frequency combs which aids in measuring optical frequency in a very stable manner and also produces multiple reference frequencies [4]. Developing an optical atomic clock eventuates from trapping atoms or ion in a well characterized and controlled environment followed by reduction of their thermal energy using laser cooling. Once the secular motion of the trapped species is minimized, the clock transition is probed using a laser, then the resulting spectroscopic signal is used as feedback to stabilize the clock laser frequency. The stabilized laser frequency is up/down-converted using combination of optical frequency comb and electronic hardware to finally use it for defining 1 second or use its different frequencies as reference for other applications [4].

The optical atomic clocks are mainly divided into two types on the basis of trapped species. Optical lattice clock that is based on trapped atoms in an optical lattice and the other one is trapped ion optical clock based on single ion trapped using electric field [6]. Both types of clock have their own advantage/disadvantage, which brings various challenges to the experimentalist in building a precise optical atomic clock. The optical lattice clock has a better signal to noise (S/N) ratio because of larger number of trapped atoms as compared to the single ion based atomic clock. Optical clock based on single ion allows longer measurement time than the lattice clock because a single ion is isolated from external environmental interaction and stored within the trap for much longer period [4], whereas in lattice clock, there is a possibility of atom-atom interaction that affects the reproducibility of the measurement. Low laser power is required in ion based clocks while periodically arranged potentials for trapping neutral atoms in optical lattice clocks require high power lasers [7]. As a result of that total power consumption in the setup is much lower, which suits for specific applications like space grade clock. Other than that, the trapping of ion and controlling its motion is comparatively easier than manipulating large number of neutral atoms. Miniaturization of ion based optical clocks is possible because of its less complex experimental requirements and rather simple design as compared to higher complications in lattice clocks. The lifetime of trapped ion is very large than that of trapped atoms, which decreases the time period of use of atomic reservoir in ion-trap based clocks. The low S/N ratio in ion based clock is possible to overcome by using low noise detector, e.g., CCD



Fig. 1.2: Projected evolution of commercial atomic clocks those are either presently available or in the development phase.

cameras and photo multipliers together with high resolution imaging objective. Also, in future there might be a concept of hybrid trap [8] which will assemble the technique of trapping by electric fields and optical lattice to trap multiple ions for good S/N ratio. There is a possibility of new generation of commercial optical clocks using single or multiple ion traps, which may be used in applications requiring lesser accuracy but higher than the present microwave based atomic clocks [9, 10]. The possibilities and present scenario for new kind of chip based atomic clock are presented in Fig. 1.2. It is desired that small and chip based optical atomic clocks will be used in satellite systems for more accurate time synchronization. A review on different types of singly charged trapped ion optical clocks together with their advantages, disadvantages and scientific potentials are described in our earlier publication [11]. At CSIR-NPL, which is the metrology institute of India, we are developing an optical atomic clock based on single, laser cooled Ytterbium-171 ion. In Fig. 1.1, actual picture of the Yb ion optical clock laboratory is depicted. This work is motivated to meet the future needs of the high end technologies and uplift our nation at par to the international standard as there is always a requirement of better clock for scientific experiments and technology demands. The objective of our experiment is to trap a single Yb ion (Yb⁺) for optical frequency standard, which may be used as secondary time & frequency standard until redefinition of the second in SI unit system.

1.1. Ytterbium-ion optical frequency standard

In optical atomic clock, choice of the technique to probe clock transition is not the only parameter which decides the stability of the reference frequency generation. The choice of species whose one of the forbidden optical transition we want to trap and probe, is the crucial part of the experiment. The choice of the experimental species depends on many parameters such as, life time of the clock states, commercial availability of lasers used in experiment, ionization potential of the neutral atom, sublimation energy requirement for atomic vapour, percentage of insensitiveness of the clock states in presence of external perturbation and so on. Considering all these parameters, and viability of creating an optical atomic clock international committee for weights and measures (CIPM) listed few atoms and ions [12], whose clock transitions may be used for redefinition of the SI second. Among these listed atoms and ions, we choose Yb⁺ and use its clock transition as reference to build optical atomic clock. To excite various transitions of Yb⁺, it requires lasers at wavelengths 399 nm, 369 nm, 935 nm; 760 nm and 467 nm for ionizing, cooling, repumping and probing clock transition, respectively [13]. These lasers and their respective optics are easily commercially available. RF based endcap quadrupole ion trap are the suitable ones for trapping single ion as they provide enough optical access to the trap centre, which is required for shining all different lasers [14]. For trapping and probing single ion in isolation, ultra high vacuum on the order of 10^{-12} torr is required. In this range of vacuum, remaining hydrogen atoms chemically react with Yb⁺ and form hydride which decreases the life time of the trapped ion. However, using Yb⁺ is advantageous as ytterbium hydride easily photodissociates to Yb⁺ in presence of cooling laser at 369 nm and gets retrapped [15]. This makes Yb⁺ a better candidate for ion based atomic clock as the frequent ion loading is not needed. Yb⁺ provides three different clock transition states with life times 7.2(3) ms [16], 61(6)ms [17] and about 8.4 year [18] with an additional microwave clock transition between hyperfine doublet ground states at 12.6 GHz [19], which is one of the primary reasons to choose Yb⁺ for our experiment. The energy level diagram of Yb⁺ indicating all clock transitions, E1 transitions used for cooling and repump transitions are shown in Fig. 1.3. Various perturbations occurring around the trapped ion results in shift in the clock states and creates uncertainty in the measurement. Some of these systematic shifts are electric field leading to Stark shift, Black Body Radiation shift, light shift, effect of magnetic field causing Zeeman shift, second order Doppler shift and etc. Such shifts arising from various sources are called systematic shifts of the atomic clock. We choose ¹⁷¹Yb isotope because it has a nuclear spin I = 1/2, which couples with the electron spin and produces F = 0 hyperfine sublevels at both ground and excited states associated to the clock transition. As a result of that the clock transitions are insensitive to the first order Zeeman shift. Trapped Yb⁺ based clock is widely used for many other experiments like formation of qubits [20] and for measuring temporal consistency of the fundamental constants. Yb⁺ and its clock transition $|^{2}S_{1/2}, F = 0 \rangle \rightarrow |^{2}F_{7/2}, F = 3 \rangle$ is more sensitive for the measurement of consistency of the Fine structure constant [21, 22]; it is in fact 100 times more sensitive than Sr based best optical lattice clock [23]. In addition to this, $|{}^{2}F_{7/2}, F = 3\rangle$ state has large enhancement for measuring violation of local Lorentz symmetry [24]. These promote Yb⁺ as the suitable candidate for our experiment which allows to study wide variety of physics problems.



Fig. 1.3: Energy level diagram of Yb ion, where we show all transitions that can be used for laser cooling and clocks. Splitting between hyperfine states are given in GHz.

We are setting up Yb⁺ clock experiment at the 467 nm $|^{2}S_{1/2}, F = 0\rangle \rightarrow |^{2}F_{7/2}, F = 3\rangle$ electric octupole clock transition. Starting point of our Yb⁺ based experiment is its production. In the presence of 399 nm and 369 nm lasers, the outer electron of the Ytterbium atom gets excited and stripped off from the atomic potential, thus photoionizing it and producing Ytterbium ions. Once the atoms get ionized, they get trapped in the harmonic potential generated by combination of AC and DC electric fields. Using 369.5 nm, the E1 transition between ground $|^{2}S_{1/2}, F = 1\rangle$ to $|^{2}P_{1/2}, F = 0\rangle$ states is used for laser cooling and probing ions by fluorescence measurement. From the excited $|^{2}P_{1/2}, F = 0\rangle$ state, there is a finite probability of the ion leaking from the cooling cycle and getting de-excited to $|^{2}D_{3/2}, F = 1, 2\rangle$ or $|^{2}F_{7/2}, F = 3, 4\rangle$ states, which have longer lifetimes. As a result of that the laser cooling of the ion stops. To pump the ion back to the cooling cycle, additional repump lasers of wavelength 935 nm and 760 nm are used to create a closed cycle. When ion goes back to the ground state, there is a finite probability of the ion reaching to another hyperfine ground state $|^{2}S_{1/2}, F = 0\rangle$. To overcome

Energy states	$\Delta \mathrm{E} \ \mathrm{(cm^{-1})}$	$\begin{array}{c} \mathbf{A}_{ik} \\ (\mathbf{s}^{-1}) \end{array}$	Life time	Hyperfine splitting (GHz)	References
$ 4f^{14}6s~^2S_{1/2},F=0,1\rangle$	-	-	-	12.645(2)	[20]
$ 4f^{14}6p~^2P_{1/2},F=0,1\rangle$	27 061.82	1.23×10^8	8.07(9) ns	2.105(1)	[25, 26]
$ 4f^{14}5d~^2D_{3/2},F=1,2\rangle$	22 960.80	16.39	$61(6) \mathrm{ms}$	0.86(2)	[17, 20]
$ 4f^{14}5d~^2D_{5/2},F=3,2\rangle$	24 332.69	138.89	7.2(3) ms	0.1909(2)	[16, 27]
$ 4f^{13}5d6s \ ^{3}D[3/2]_{1/2}, F = 0, 1\rangle$	33 653.86	2.61×10^{7}	37.7(5) ns	2.210(1)	[28]
$ 4f^{13}5d6s \ ^{1}D[3/2]_{3/2}, F = 0, 1\rangle$	34 575.37	3.42×10^{7}	28.6(1) ns	9.1(1)	[29]
$ 4f^{13}5d6s~^2F_{7/2},F=3,4\rangle$	21 418.75	-	8.4 year	3.62(1)	[18, 29]

Tab. 1.1: Energy levels and their spectroscopic information that are relevant for our experiment.

this, we use 369 nm + 14.7 GHz as hyperfine repump which brings the ion back from F = 0to F = 1 state of the ground level. For similar reasons, we require sidebands of the repump lasers too, such as additional laser frequencies at 935 nm + 3.07 GHz, and 760 nm + 12.6 GHz. The shift in frequency of the laser light is done by optical modulators like Acousto optic modulator (AOM) and Electro optic modulator (EOM). Subsequent to laser cooling, the next process is state preparation in which we use 369.5 nm + 2.1 GHz laser frequency in presence of repump lasers to optically pump the ion at the ground state $|^{2}S_{1/2}, F = 0\rangle$, so that the clock transition is ready to be excited using 467 nm laser. During driving of the clock transition, the cooling laser together with the 760 nm repump lasers are also operating to ensure excitation of the clock transition by electron shelving technique. Absence of any fluorescence signal at this stage ensures that the probed clock laser frequency is equal to the Yb⁺ clock transition. As previously mentioned, Yb⁺ affords three different clock transitions which are $|^2S_{1/2}, F = 0 \rangle \rightarrow 0$ $|{}^{2}F_{7/2}, F = 3\rangle \ (E3), \ |{}^{2}S_{1/2}, F = 0\rangle \ \rightarrow \ |{}^{2}D_{3/2}, F = 2\rangle \ (E2) \ \text{and} \ |{}^{2}S_{1/2}, F = 0\rangle \ \rightarrow \ |{}^{2}D_{5/2}, F = 2\rangle$ (E2'). Among these three transitions only E3 and E2 clock transitions are favorable for generating stable frequency source because of their long life times hence low natural linewidths of 3 Hz and 3 nHz, respectively. In our experiment, we are interested about the E3 transition. The details of energy levels and relevant spectroscopic information which will be used to develop optical atomic clock are tabulated in Tab. 1.1.

1.2. Experimental challenges

To setup the described trapped ion optical frequency standard, we have mainly five leading challenges to be effectuated: achieving ultra high vacuum, designing and developing of a precision ion trap, electronic instrumentation, optical setups and stabilized ultra narrow clock laser. Each of these requisites are further assortment of several building blocks which are challenging to develop and construct. In our experiment, we aim for achieving ultra high vacuum of the order of 10^{-12} mbar, which is desired for increasing the trapping lifetime. To achieve this order of vacuum requires several different vacuum pumps like turbo pump, ion pump and Tisublimation pump along with proper choice of metals, electroplating and baking of the system for the vacuum chamber. The design and fabrication of a precision ion trap is also challenging because an inaccurate design will introduce trap induced systematic shifts to the clock transition. A detailed calculation is thus necessary prior to designing of the trap. Confinement of ion using the trap requires a high voltage RF with nearly-absent higher order harmonics, with which the trap electrodes are biased to create a quadrupole trapping potential. We also require a low noise DC voltage source for biasing compensation electrodes to control the ion motion. There is also a requirement of an atomic oven which would provide nearly collimated beam of neutral atoms to be ionized and trapped at the trap centre. Here, the challenge is to design an oven which produces less divergent atomic beam with small cross section so that no patch potential is created due to atoms sticking to the electrodes. Otherwise, a quadrupole shift produced due to this patch potential will lead to an inaccurate measurement of the clock frequency.

The heart of the optical frequency standard is the lasers which are used in the experiment for ionization, cooling, repumping and clock transition probing and thus stabilization of the laser frequencies are essential. The stabilization of ionizing, cooling and repump lasers require a servo controller to lock its central frequency with respect to a reference point and to prevent the system from electronic pickup noises which brings destabilization in laser frequency. The reference point is generally an atomic transition spectra in saturation absorption spectroscopy or a stabilized Fabry perot cavity with respect to any reference laser frequency. Stabilization of clock laser requires extra effort as the narrow linewidth of the probing transition demands a narrow and stable probing frequency. An ultrastable reference point in the form of peak of a transmission/reflected mode from a high finesse Ultra Low Expansion (ULE) optical cavity is utilized. Long term frequency stabilization of these lasers during experiment is an important requisite. The designing of low noise and fast servo controller for stabilizing the laser frequency is therefore essential. The servo controller is an assemble of many sophisticated electronic modules like function generator, lock-in amplifier, PID controller. All these modules require high bandwidth and low noise for stabilizing laser frequency of the clock laser and moderate bandwidth for other lasers as per our experimental requirement. Optical setup for

laser locking requires modulation in laser frequency, which is done by Acousto and Electro optical modulators. These modulators require low noise, stabilized and additionally computer controlled RF drivers to fulfill our experimental requirements. Stabilization of laser intensities is also an important part and this is dealt with using AOM and RF drivers which require additional feedback control of the RF power. In the overall experimental process, we require five different lasers, ten optical modulators and few mechanical shutters for providing correct frequency at a correct sequence during the experiment at run. For generating correct frequency during experiment, we have to stabilize laser with respect to different references like ionizing laser is stabilized using wavelength-meter, cooling and repump lasers are stabilized by transfer cavity locking technique using Rb transition spectra. The stabilizing of clock laser requires an extra effort for generating ultra-narrow linewidth & ultrastable frequency using ULE cavity and fast servo controller. Also correct optical modulation and correct sequencing for shutters' operation are required to stabilize their drivers using reference frequency from commercial atomic clocks. Hence, time difference between each events during cooling and probing clock transition is so fast that it is not desired to operate whole system manually, which creates a requirement of a precise automation unit for controlling system through computer. For this, we need to have control on tuning the laser frequencies, fast switch -on-off of the laser light, DC voltage on compensating electrodes, control on magnetic field at different stages, atomic oven ON/OFF timing and recording of the data from different sources. There is also requirement of choice of communication protocol between computer and electronic modules so that we minimize the processing delays during data transfer. This thesis describes many of these mentioned instrumentation which were worked upon during pursual of my PhD thesis.

1.3. Objective and outcome of the thesis

Developing the Yb⁺ optical atomic clock is an ensemble of many different blocks, such as, theoretical calculations, wide range of instrumentation and finally meaningful analysis of data. In case of a precision spectroscopy, many systematic shifts due to quantum perturbations increase the uncertainty budget which had to be eliminated or corrected. Due to this reason, complete theoretical knowledge about the systematic shift is imperative in addition to finding feasible solution to minimize the effect of the shifts to help in accurate frequency measurement. Based on our experimental purpose, many instrumentations such as mechanical shutters, current source, voltage source, AOM/EOM drivers, magnetic shielding coils, optical systems, *etc.* are also part of the experiment. Controlling a wide range of such modules in simultaneous and synchronous manner require an automation of the experiment. Developing of a trapped ion optical frequency standard is long term goal of the lab. Within that frame work, the described work in this thesis starts with theoretical calculation of the photon assisted systematic shifts and then describe about the automation and indigenously developed instruments like voltage source, address module, servo controller and RF drivers. We also describe a novel technique to generate reference point for stabilizing laser frequencies which are required in this experiment.

In Chapter 2, we are focusing on the Yb-ion system and used various theoretical tools such as, relativistic coupled-cluster (RCC) method and relativistic many-body perturbation theory (MBPT) to calculate the electric dipole (E1) transition matrices associated to ground and excited states of the clock transition. Finally, using the results obtained by these methods, *i.e.* wave function of the desired states, we calculate ac and dc polarizabilities of the states associated to the clock transition. Magic and tune-out wavelengths have been estimated that could be of interest to cancel out light shifts. Magic wavelength are useful to construct array of Yb-ions stored in an optical lattice where the clock transition frequency remains unperturbed. As application of this calculation, it gives values of polarizability and estimate BBR shift which is one of the major contributors in systematic shifts.

In Chapter 3, we describe the address module used for automation in our experiment. This module is hooked up with every indigenously developed electronic instruments so that they can be controlled via a computer. This module provides unique address to each instrument so that all can be controlled using a universal data bus thus avoiding the mess of wires around the experimental setup. We have developed a computer controlled, regulated DC voltage source for biasing the compensating electrodes and controlling the ion position. Such computer controlled ultra-stable high voltage sources tunable to any arbitrary waveforms at low frequencies have many other applications as well. Details of the DC source is also described in this chapter.

In Chapter 4, we describe indigenously developed servo controller for laser frequency stabilization. Such a servo system requires function generator, lock-in amplifier and PID controller for laser frequency stabilization to an absolute reference. These modules are programmed on an FPGA using VHDL language. The developed integrated digital servo controller is very compact, portable and also free from any electronic pickup noise. Characterization of the modules in terms of filter frequency response, PID bandwidth *etc.* are done using different measuring instruments. We have also added an auto relock mechanism to our PID module which makes the instrument smart and efficient. Using our developed algorithm, the system itself monitors its stability and in cases of slipping from the lock point, it automatically searches and relocks to the setpoint.

In Chapter 5, we have developed optical setups for providing absolute reference point with respect to an atomic transition to the servo controller to lock laser frequency. In our experiment, for photoionization and repump lasers at 935 nm and 760 nm wavelength no such atomic references are available so that simple pump-probe spectroscopy can be done for locking these

lasers. We use wavelength meter for locking photoionizing laser as it requires only short term frequency stability. For repump lasers, we use transfer cavity locking technique where we first lock a master laser using an atomic transition, with respect to which the Fabry-Perot cavity is stabilized. The stabilized cavity transmission spectrum is then used as a reference for the repump lasers for locking their frequency. In this technique, the stability of master laser is transferred to slave laser through an intermediate Fabry Perot cavity.

We require several laser beams at shifted frequencies to drive multiple transitions between different hyperfine levels, which is done using AOMs and EOMs. These modulators require stabilized RF sources within the range 80 MHz to 3 GHz to drive them. In chapter 6, we describe the design, working principle and characterize these drivers.

Finally in Chapter 7, I give conclusion of this thesis and possible future works towards developing Yb-ion optical frequency standard.

Chapter 2

Polarizability, Magic and Tuneout Wavelength Calculation of Yb⁺ Clock Transition

Publication: A. Roy, S. De, Bindiya Arora and B. K. Sahoo, Accurate determination of blackbody radiation shift, magic and tune-out wavelength for the $6S_{1/2} \rightarrow 5D_{3/2}$ clock transition in Yb⁺, J. Phys. B: At. Mol. Opt. Phys. **50**, 205201 (2017).

2.1. Introduction

Trapped and laser cooled ions are exciting for studying wide variety of quantum mechanical phenomena such as simulating many-body systems, computation and information processing, quantum sensors and metrology. Since invention of the Paul and Penning traps, enormous efforts have been made for studying such phenomena using trapped ion systems where laser cooling enhances possibility of probing the quantum dynamics by overcoming thermal energy of the ions. Optical trapping of ions and storing them in optical lattices have been less investigated so far as compared to the neutral atoms and are promising since micromotions are not present there, which is otherwise unavoidable in any electrodynamic trap. Ions stored in an optical lattice can be manipulated to different geometries such as linear chain, zig-zag ladder, triangular and so on, which opens up possibility of studying novel quantum phases [30], realization of micro-trap systems for quantum information processing [31], [32], effective spin-spin interaction mediated by long range Coulomb interaction and quantum magnetism in an ion chain [33], [34]. In our experiment, we use Ytterbium-ion (Yb⁺) for trapping and probing its clock transition. The two $\label{eq:2.1} {\rm quadrupole\ (E2)\ transitions,\ [4f^{14}6s]\ ^2S_{1/2} \rightarrow [4f^{14}5d]\ ^2D_{3/2}\ {\rm and\ [4f^{14}6s]\ ^2S_{1/2} \rightarrow [4f^{14}5d]\ ^2D_{5/2},}$ and one octupole (E3) transition, $[4f^{14}6s] {}^{2}S_{1/2} \rightarrow [4f^{13}6s^{2}] {}^{2}F_{7/2}$, of Yb⁺ as shown in Fig. 2.1 are being undertaken for the optical frequency standards [35, 36, 37, 38, 5]. In this chapter, we have shown the possibilities of multiple ions trapped in optical lattice to probe one of its clock transition $[4f^{14}6s] {}^{2}S_{1/2} \rightarrow [4f^{14}5d] {}^{2}D_{3/2}$. The possibilities of building such a system is judged by its systematics shifts, which creates uncertainties in the frequency measurements. For precise frequency measurements, knowledge of the major source of systematics shifts like differential black-body radiation (BBR) shifts and light shifts are important. Theoretical estimation of these shifts are crucial factors for deciding the uncertainties of the clock transitions. Accurate estimation of these quantities demands a reliable knowledge of the scalar components of the static polarizabilities. Precise values of the static scalar polarizabilities of the atomic states associated with the [4f¹⁴6s] ${}^{2}S_{1/2} \rightarrow [4f^{14}5d] {}^{2}D_{3/2}$ clock transition in Yb⁺ is required in calculation of light shift of clock state which gives information regarding reduction of this effect using a specific wavelength called magic wavelength. At this wavelength, the differential shift of ground and excited state of a clock transition goes to zero. The uncertainty in our measurement due to this shift reduces if we get correct knowledge about the geometry of the lattice used for trapping multiple ions. In this chapter, we describe thoroughly about the calculations used for polarizability and coulomb shift, which gives evidence of the possibilities of multiple Yb⁺ trap in optical lattice for the [4f¹⁴6s] ${}^{2}S_{1/2} \rightarrow [4f^{14}5d] {}^{2}D_{3/2}$ clock transition. This might open up developing multi-ion optical clock in future as this is of interest within the community [39].

2.2. Theory of polarizability

In an atomic energy state, the Stark shift of the n^{th} hyperfine level with angular momentum F_n and its magnetic component M_{F_n} is introduced when it is placed in a uniform oscillating electric field of a laser beam

$$\mathcal{E}(t) = \frac{1}{2} \mathcal{E}\hat{\varepsilon} e^{-(\iota \omega t - \mathbf{k}.\mathbf{r})} + c.c., \qquad (2.1)$$

with \mathcal{E} being the amplitude, $\hat{\boldsymbol{\varepsilon}}$ is the polarization vector of the electric field and *c.c.* referring to the complex conjugate of the former term and oscillating at frequency ω given by

$$\Delta E_{F_n}^{\text{Stark}} \approx -\frac{1}{4} \alpha_{F_n}(\omega) \mathcal{E}^2, \qquad (2.2)$$

where $\alpha_{F_n}(\omega)$ is the frequency dependent or dynamic polarizability of the state and is given by

$$\alpha_{F_n}(\omega) = \alpha_{F_n}^{(0)}(\omega) + \alpha_{F_n}^{(1)}(\omega) \frac{A\cos\theta_k M_{F_n}}{2F_n} + \alpha_{F_n}^{(2)}(\omega) \left(\frac{3\cos^2\theta_p - 1}{2}\right) \\
\times \left[\frac{3M_{F_n}^2 - F_n(F_n + 1)}{F_n(2F_n - 1)}\right].$$
(2.3)

Here $\alpha_{F_n}^{(i)}(\omega)$ are the scalar, vector and tensor components of the frequency dependent polarizability for i = 0, 1, and 2 respectively, A represents degree of polarization, θ_k is the angle between quantization axis and wave vector, and θ_p is the angle between quantization axis and direction of polarization. In presence of magnetic field, $\cos \theta_k$ and $\cos^2 \theta_p$ can have any values depending on the direction of applied magnetic field. In absence of magnetic field, $\cos \theta_k = 0$ and $\cos^2 \theta_p = 1$ for linearly polarized light, where polarization vector is assumed to be along the quantization axis. However, it yields $\cos \theta_k = 1$ and $\cos^2 \theta_p = 0$ for circularly polarized light, where the wave vector is assumed to be along the quantization axis. Here, we present results for both polarizations of the light assuming absence of the magnetic field. The component of the polarizability can be expressed as [40, 41].

$$\alpha_{F_n}^{(0)}(\omega) = \alpha_{J_n}^{(0)}(\omega),$$
(2.4)

$$\alpha_{F_n}^{(1)}(\omega) = (-1)^{J_n + F_n + I + 1} \left\{ \begin{array}{cc} F_n & J_n & I \\ J_n & F_n & 1 \end{array} \right\} \sqrt{\frac{F_n(2F_n + 1)(2J_n + 1)(J_n + 1)}{J_n(F_n + 1)}} \\ \times \alpha_{J_n}^{(1)}(\omega) \qquad (2.5)$$

and

$$\alpha_{F_n}^{(2)}(\omega) = (-1)^{J_n + F_n + I} \left\{ \begin{array}{ccc} F_n & J_n & I \\ J_n & F_n & 2 \end{array} \right\} \sqrt{\frac{F_n(2F_n - 1)(2F_n + 1)}{(2F_n + 3)(F_n + 1)}} \\
\times \sqrt{\frac{(2J_n + 3)(2J_n + 1)(J_n + 1)}{J_n(2J_n - 1)}} \,\alpha_{J_n}^{(2)}(\omega) \qquad (2.6)$$

Here, J_n is the total angular momentum of the atomic state, I is the nuclear spin and $\alpha_{J_n}^{(i)}$ for i = 0, 1, 2 are the scalar, vector and tensor components of the atomic dipole polarizability. Mathematical expressions of these polarizability components as discussed in Ref. [42], are given as,

$$\alpha_{J_n}^{(0)}(\omega) = \frac{2}{3(2J_n+1)} \sum_{k \neq n} \frac{(E_n - E_k) |\langle J_n || \mathbf{D} || J_k \rangle|^2}{\omega^2 - (E_n - E_k)^2},$$
(2.7)

$$\alpha_{J_n}^{(1)}(\omega) = \sqrt{\frac{24J_n}{(J_n+1)(2J_n+1)}} \sum_{k \neq n} (-1)^{J_n+J_k} \begin{cases} J_n & 1 & J_n \\ 1 & J_k & 1 \end{cases} \\ \times \frac{\omega|\langle J_n||\mathbf{D}||J_k\rangle|^2}{\omega^2 - (E_n - E_k)^2}$$
(2.8)

$$\alpha_{J_{n}}^{(2)}(\omega) = \sqrt{\frac{40J_{n}(2J_{n}-1)}{3(J_{n}+1)(2J_{n}+3)(2J_{n}+1)}}} \sum_{k \neq n} (-1)^{J_{n}+J_{k}} \begin{cases} J_{n} & 2 & J_{n} \\ 1 & J_{k} & 1 \end{cases} \\ \times \frac{(E_{n}-E_{k})|\langle J_{n}||\mathbf{D}||J_{k}\rangle|^{2}}{\omega^{2}-(E_{n}-E_{k})^{2}}, \quad (2.9)$$

where J_m and E_m are the total angular momentum and energy, respectively, for m = n, kstates and $\langle J_n || \mathbf{D} || J_k \rangle$ are the reduced E1 matrix elements. The trapped atom is polarised not only by laser field or DC electric field but also due to the oscillation of electric field from black body radiation, which produces a systematic shift called BBR shift. The source of this shift is the temperature at position of the trapped ion which polarizes the atomic state and produces systematic shift. This shift can be reduced by decreasing the environmental temperature, which is a difficult task and other option is to estimate BBR shift value. For calculation of BBR shift, we approximate that, the polarizability of atomic state due to thermal radiation near the atom position is equal to the static polarization. This is because, at nearly room temperature, the thermal radiation frequencies from black body spectrum is very small compared to E1 transition frequencies of Yb ion which are considered in calculations of polarization of atomic states. The expression of BBR shift is [43]

$$\Delta E_F^{BBR} \simeq -\frac{1}{2} (831.9 \text{ V/m})^2 \left[\frac{T(\text{K})}{300}\right]^4 \alpha_F^{(0)}(0) (\mathbf{1} + \eta), \qquad (2.10)$$

where η is the dynamic fractional correction factor and is defined as [43]. This dynamic correction comes due to frequency distribution of black body radiation field. To calculate η , we use electric dipole matrix element of that transition, whose contribution is dominant in polarizability calculations.

$$\eta = \frac{(80/63)\pi^2 T^2}{(J_n + 1)\alpha_F^{(0)}(0)} \sum_k \frac{|\langle J_n || \mathbf{D} || J_k \rangle|^2}{(E_n - E_k)^3} \left(1 + \frac{21\pi^2 T^2}{5(E_n - E_k)^2} + \frac{336\pi^4 T^4}{11(E_n - E_k)^4} \right).$$
(2.11)

The differential BBR shift of a transition is, thus, the difference of the BBR shifts of the states associated with that transition. We use atomic units (a.u.) throughout the article unless stated otherwise. The α_{F_n} values can be converted from a.u. to Hz m²V⁻² and Jm²V⁻² by dividing with factors 2.48832 × 10⁻⁸ and 6.064 × 10⁴⁰, respectively. The differential AC-Stark shift for a transition between states with hyperfine moments F and F' is given by

$$\delta(\Delta E_{FF'}^{Stark}) \approx \Delta E_F^{Stark} - \Delta E_{F'}^{Stark} = -\frac{1}{2} \delta \alpha_{FF'}(\omega) \mathcal{E}^2(\omega).$$
(2.12)

In order to find out λ_{magic} of a transition, it is imperative to identify the corresponding values of ω at which the differential Stark shift $\delta(\Delta E_{FF}^{Stark})$ is zero for any finite electric field strength \mathcal{E} . By suitably choosing polarization of the electric field and azimuthal sublevels, λ_{magic} can be determined for different hyperfine levels of a transition. In fact, we had demonstrated in a



Fig. 2.1: Energy level diagram of Yb⁺: All the clock transitions and low-lying states that are relevant for the experimental set-up and demonstrating role of their importance in calculating dipole polarizabilities of the [4f¹⁴6s] ${}^{2}S_{1/2}$ and [4f¹⁴5d] ${}^{2}D_{3/2}$ states are highlighted here.

recent work that by suitably deciding a trap geometry, the Stark shift of an atomic state or differential Stark shift of a transition can be obtained which can be independent of the vector and tensor components of the states involved [44]. Assuming that such a trapping configuration can be achieved for trapping the Yb⁺ ion, we also give λ_{magic} for the aforementioned clock transition. This comprehensive trapping scheme could be useful for avoiding hyperfine level selective trapping of ¹⁷¹Yb⁺. We also evaluate λ_{T} values for which $\alpha_F(\omega)$ can become zero independently for the [4f¹⁴6s] ²S_{1/2} and [4f¹⁴5d] ²D_{3/2} states.

2.3. Method of calculations

When atom is polarized due to electromagnetic wave, the distribution of electrons rotating around the nucleus is changed. Due to this change in configuration of electrons, we divide contribution of each $\alpha_{J_n}^{(i)}$ components broadly into three parts, as described in Refs. [45, 46], which is expressed as

$$\alpha_{J_n}^{(i)} = \alpha^{(i,c)} + \alpha_{J_n}^{(i,cv)} + \alpha_{J_n}^{(i,v)} \quad .$$
(2.13)

The superscripts c, cv and v in the parentheses are known as core (independent of J_n), corevalence and valence contributions, respectively [45, 46]. When the core of the electron distribution has an inert gas configuration, core and core-valence contributions usually come out to be much smaller than the valence contribution [47, 45, 46, 48]. This has also been observed earlier in the ground state polarizability study of Yb⁺ [48]. Again, it is possible to use the reduced E1 matrix elements among the low-lying bound states directly in Eqs. (2.7-2.9) in a sum-over-states approach to estimate the dominant contribution to $\alpha_{J_n}^{(i,v)}$. We refer to this as "Main" valence correlation contribution and the rest that comes from the other higher level excited states as "Tail" contribution to $\alpha_{J_n}^{(i,v)}$.

For an accurate determination of the E1 matrix elements between many low-lying states, we employ the relativistic coupled-cluster (RCC) method by expressing the wave function $(|\Psi_v\rangle)$ of an atomic state that has the closed core [5p⁶] and a valence orbital v as

$$|\Psi_v\rangle = e^T \{1 + S_v\} |\Phi_v\rangle \tag{2.14}$$

where $|\Phi_v\rangle$ is a reference state, which is defined as $|\Phi_v\rangle = a_v^{\dagger}|\Phi_0\rangle$ with the Dirac-Hartree-Fock (DHF) wave function $|\Phi_0\rangle$ of the closed-core, T and S_v are the RCC excitation operators that excite electrons from the core and core along with the valence orbital respectively. In this work, we have only accounted for the single and double excitations which are denoted using the subscripts 1 and 2 respectively in the RCC operators as

$$T = T_1 + T_2$$
 and $S_v = S_{1v} + S_{2v}$. (2.15)

These are known as coupled-cluster singles and doubles (CCSD) method in the literature. Amplitudes of these operators are evaluated using the equations

$$\langle \Phi_0^* | \overline{H} | \Phi_0 \rangle = 0 \tag{2.16}$$

and

$$\langle \Phi_v^* | (\overline{H} - E_v) S_v | \Phi_v \rangle = -\langle \Phi_v^* | \overline{H} | \Phi_v \rangle, \qquad (2.17)$$

where $|\Phi_0^*\rangle$ and $|\Phi_v^*\rangle$ are the excited state configurations, here up to doubles, with respect to the DHF states $|\Phi_0\rangle$ and $|\Phi_v\rangle$, respectively, and $\overline{H} = (He^T)_l$ with subscript *l* representing the linked terms only. E_v is the energy eigenvalue of the $|\Psi_v\rangle$ state and is determined by using the expression

$$E_v = \langle \Phi_v | \overline{H} \{ 1 + S_v \} | \Phi_v \rangle. \tag{2.18}$$

Both Eqns. (2.17) and (2.18) need to be solved simultaneously for obtaining solutions of interdependent variables in a self-consistent approach. At the same level of approximation in the excitations, a truncated RCC method can incorporate more contributions as compared to a truncated configuration-interaction (CI) method. For example, the lowest order mixing of $[4f^{13}5d6s]$ ³[3/2] with J = 1/2 configuration and the DHF wave function of the $[4f^{14}6p]$ ² $P_{1/2}$ configuration is carried out by the S_{2v} operator while calculating the $[4f^{14}6p]$ ² $P_{1/2}$ atomic state function. Similar mixing also takes place among J = 3/2 configurations. Moreover, \overline{H} in the CCSD method contains many non-linear terms that can take into account contributions from the higher level excitations, such as triples and quadruples, at the same level of excitation approximation of the CI method. As a matter of fact, the above mentioned configuration mixing are incorporated through the non-linear terms of the CCSD method and they take care of higher order correlation contributions. This is a unique feature of a truncated RCC method in contrast to other truncated many-body methods owing to the exponential form of expression in Eq. (2.14). In fact, it has also been employed to calculate many atomic properties of Yb⁺ accurately in the past [49, 50].

After obtaining amplitudes using the above equations, the transition matrix element of the dipole operator D between the states $|\Psi_i\rangle$ and $|\Psi_f\rangle$ is evaluated using the expression

$$\frac{\langle \Psi_f | D | \Psi_i \rangle}{\sqrt{\langle \Psi_f | \Psi_f \rangle \langle \Psi_i | \Psi_i \rangle}} = \frac{\langle \Phi_f | \tilde{D}_{fi} | \Phi_i \rangle}{\sqrt{\langle \Phi_f | \{1 + \tilde{N}_f\} | \Phi_f \rangle \langle \Phi_i | \{1 + \tilde{N}_i\} | \Phi_i \rangle}},$$
(2.19)

where $\tilde{D}_{fi} = \{1 + S_f^{\dagger}\}e^{T^{\dagger}}De^T\{1 + S_i\}$ and $\tilde{N}_{k=f,i} = \{1 + S_k^{\dagger}\}e^{T^{\dagger}}e^T\{1 + S_k\}$. The above expression involves two non-terminating series in the numerator and denominator, which are $e^{T^{\dagger}}De^T$ and $e^{T^{\dagger}}e^T$ respectively, and calculation of these terms are described in our previous works [50]. The "Main $(\alpha_{J_n}^{(i,v)})$ " contributions have been calculated by combining the E1 matrix elements obtained using the above expression with the experimental energies as listed in Ref. [51]. Contributions from higher excited states including continuum to $\alpha_{J_n}^{(i,v)}$, denoted as "Tail $(\alpha_{J_n}^{(i,v)})$ ", are estimated approximately in the DHF method using the expression

$$\alpha_{J_n}^{(i,v)}(\omega) = \frac{2}{3(2J_n+1)} \sum_{K>n} \frac{(\epsilon_K - \epsilon_n) |\langle J_n || \mathbf{D} || J_K \rangle_{DHF} |^2}{(\epsilon_n - \epsilon_K)^2 - \omega^2},$$
(2.20)

where $\langle J_n || \mathbf{D} || J_K \rangle_{DHF}$ are obtained using the DHF wave functions, K > n corresponds to the excited states including continuum whose matrix elements are not accounted in the Main $(\alpha_{J_n}^{(i,v)})$ contribution, and $\epsilon_{n,K}$ refers to the DHF energies. Similarly, the core-valence contributions $\alpha_{J_n}^{(i,cv)}$ are also obtained through the DHF method approximation using the expression

$$\alpha_{J_n}^{(i,cv)}(\omega) = \frac{2}{3(2J_n+1)} \sum_{K}^{N_c} \frac{(\epsilon_K - \epsilon_n) |\langle J_n || \mathbf{D} || J_K \rangle_{DHF} |^2}{(\epsilon_n - \epsilon_K)^2 - \omega^2},$$
(2.21)

where N_c is the total number of electrons in the closed-core of the Yb⁺. The static scalar core contributions $\alpha^{(0,c)}(0)$ in Yb⁺, which is equal to the ground state polarizability of Yb²⁺, are evaluated using the DHF, random-phase approximation (RPA) and a perturbed CCSD method considering a dipole operator D as an external perturbation as described in Ref. [52]. The trends in these results demonstrate role of electron correlation effects for its accurate evaluation. The DHF and RPA contributions are subsumed within the CCSD result by the formulation of the theory and are considered as the most precise calculation. Comparison between the results of DHF and CCSD; and RPA and CCSD show small differences while the CCSD method is computationally very expensive. As a matter of fact, it is not possible to apply the CCSD method to determine core contribution to the dynamic polarizabilities for the estimates of the λ_{magic} and λ_{T} values. Therefore, we employ the DHF method to calculate the core contributions to the dynamic polarizabilities. Nevertheless, core contributions cancel out completely in the determination of λ_{magic} . Hence, accuracies in those values are not affected by the use of core correlations from the DHF method while the λ_{T} values can be estimated within reasonable accuracy.

2.4. Results and Discussion

Figure 2.1 shows the E1 transitions which are contributing in polarization of the states $[4f^{14}6s] {}^{2}S_{1/2}$ and $[4f^{14}5d] {}^{2}D_{3/2}$. Eqs.(2.7-2.9) shows the dependency of the polarizability of the atomic state upon E1 transition matrix, transition frequency and angular momentum of the energy state. We also observe from Fig. 2.1 that the $[4f^{14}6p] {}^{2}P_{1/2,3/2}$ states will contribute dominantly to the polarizabilities of both the ground and $[4f^{14}5d] {}^{2}D_{3/2}$ states. Therefore, accuracy of our result depends upon precise calculation of E1 matrix for transition between $[4f^{14}6s] {}^{2}S_{1/2} \rightarrow [4f^{14}6p] {}^{2}P_{1/2,3/2}$ and $[4f^{14}5d] {}^{2}D_{3/2} \rightarrow [4f^{14}6p] {}^{2}P_{1/2,3/2}$. In this section, we describe the result of static polarizability used for estimation of stark and BBR shift. We also show the result of magic wavelength and tune out wavelength which is calculated using dynamic polarizability of the clock states. This calculation gives the estimation of wavelength which are used for trapping ions in an optical lattice.

2.4.1 Static polarizability

In calculation of static polarization, we calculated E1 transition matrix from experimental and theoretical data for ground and $[4f^{14}5d] {}^{2}D_{3/2}$ state. The values for each E1 transition are tabulated in Tab. 2.1, where we also give contributions from the individual correlation effects to the static $\alpha_{J_n}^{(i)}$ values of the ground and $[4f^{14}5d] {}^{2}D_{3/2}$ states in 171 Yb⁺. The "Main" contributions are listed separately in the table along with the E1 matrix elements and resonant wavelengths $\lambda_{\rm res}$ of different transitions involving the low-lying $P_{1/2,3/2}$ states to illustrate their roles explicitly in the accurate evaluation of $\alpha_{J_n}^{(i)}$. Uncertainties of these matrix elements are
given along with their respective values. The uncertainties in the CCSD values are determined by estimating contributions from the neglected leading order triple excitations and due to use of the finite size basis functions in the calculation. We find that more than 90% contribution to the total static $\alpha_{J_n}^{(i)}$ values come from the [4f¹⁴6p] ${}^2P_{1/2}$ and [4f¹⁴6p] ${}^2P_{3/2}$ states in both the cases. Thus, we replace the E1 matrix elements of the $[4f^{14}6s] {}^{2}S_{1/2} \rightarrow [4f^{14}6p] {}^{2}P_{1/2,3/2}$ and [4f¹⁴5d] $^{2}D_{3/2} \rightarrow$ [4f¹⁴6p] $^{2}P_{1/2}$ transitions that are obtained from the CCSD method by the values extracted from the combination of the experimental values of the lifetimes and the corresponding branching ratios from Ref. [20, 53] of the $[4f^{14}6p] {}^{2}P_{1/2}$ and $[4f^{14}6p] {}^{2}P_{3/2}$ states. These values are quoted in **bold** fonts while the CCSD values are mentioned just below these numbers in Table 2.1. We, however, could not extract out the E1 matrix element of the $[4f^{14}5d]$ ²D_{3/2} \rightarrow $[4f^{14}6p]$ ²P_{3/2} transition from any available experimental data. Nevertheless, we find that the experimental value of the E1 matrix element, that is 2.97 a.u., corresponding to the [4f¹⁴5d] $^{2}D_{3/2} \rightarrow$ [4f¹⁴6p] $^{2}P_{1/2}$ transition is very close to our CCSD result (2.95 a.u.). Since the [4f¹⁴6p] ${}^{2}P_{1/2}$ and [4f¹⁴6p] ${}^{2}P_{3/2}$ states are the fine structure partners, electron effects will behave similarly in determining these states. Therefore, we assume that the CCSD value for the E1 matrix element of the $[4f^{14}5d]$ $^{2}D_{3/2} \rightarrow [4f^{14}6p]$ $^{2}P_{3/2}$ transition will have similar accuracy as of the corresponding value of the $[4f^{14}5d] {}^{2}D_{3/2} \rightarrow [4f^{14}6p] {}^{2}P_{1/2}$ transition. Next to the 6P states, there lie the states with $[4f^{13}5d6s]$ configurations. Finding E1 matrix elements for these states are strenuous in the RCC method, but a CI method can estimate them more reliably. We consider these values either from the experimental data presented in Ref. [55] or are taken from the theoretical calculations reported in Ref. [54] using a CI method. Since uncertainties of these matrix elements obtained from the CI method are not given and their contributions to the polarizability values are found to be relatively small (see Table 2.1), we have not accounted for uncertainties from these matrix elements in the present work. In order to infer E1 matrix element of the $[4f^{13}5d6s]$ ${}^{3}[3/2]_{1/2} \rightarrow [4f^{14}5d]$ ${}^{2}D_{3/2}$ transition for estimating its contribution in the evaluation of the polarizability of the $[4f^{14}5d]$ $^{2}D_{3/2}$ state, we have used the experimentally measured lifetime of the $[4f^{13}5d6s]^{3}[3/2]_{1/2}$ state from Ref. [55] and branching ratios reported in Refs. [56, 57].

As seen in Table 2.1, the core correlation has significant contribution to the scalar $\alpha_{J_n}^{(0)}$ values. This cannot be evaluated using a sum-over-states approach, so it is imperative to apply a suitable *ab initio* many-body method for its determination. The core correlation contribution $\alpha^{(i,c)}$ to the ground and $[4f^{14}5d] {}^{2}D_{3/2}$ states of Yb⁺, which corresponds to dipole polarizability of the Yb²⁺ ion, has not been evaluated rigorously earlier. To determine this contribution more reliably, we apply three different methods in the DHF, RPA and CCSD approximations based on the first principle calculations as discussed in Ref. [52]. We find $\alpha^{(i,c)} = 7.45$ a.u., $\alpha^{(i,c)} = 6.38$ a.u. and $\alpha^{(i,c)} = 7.72$ a.u. from the DHF, RPA and CCSD methods, respectively. In Ref. [48], the RPA value is given as 6.386 a.u. which agrees well with our RPA value. The

Tab. 2.1: Contributions to the static values of $\alpha_{J_n}^{(i=0,2)}$ for the [4f¹⁴6s] ${}^2S_{1/2}$ and [4f¹⁴5d] ${}^2D_{3/2}$ states in Yb⁺ are given in a.u. Absolute values of the dominantly contributing reduced E1 matrix elements (in a.u.) and resonance wavelengths in vacuum (λ_{res} in nm) are also listed. Values quoted in bold fonts are taken from other work as mentioned below.

Contribution	$\lambda_{\rm res}$ [51]	E1	$lpha_{J_n}^{(0)}$	$\alpha_{J_n}^{(2)}$
$\begin{array}{l} [4f^{14}6p] \ ^{2}P_{1/2} \\ [4f^{13}5d6s] \ ^{3}[3/2]_{3/2} \\ [4f^{13}5d6s] \ ^{3}[3/2]_{1/2} \\ [4f^{13}5d6s] \ ^{3}[3/2]_{1/2} \\ [4f^{13}5d6s] \ ^{1}[3/2]_{3/2} \\ [4f^{14}7p] \ ^{2}P_{1/2} \\ [4f^{14}7p] \ ^{2}P_{3/2} \\ \alpha^{(i,c)} \\ \alpha^{(i,c)}_{6s^{2}S_{1/2}} \\ Tail(\alpha^{(i,v)}_{6s\ ^{2}S_{1/2}}) \\ Total \\ Ref. \ [48] \\ P_{1}f_{1}(5) \\ [59] \end{array}$	369.52 347.73 329.03 297.05 289.14 156.97 152.45	2.471(3) [20, 53] 1.10 [54] 3.36(3) [20, 53] 0.82(2) [55, 56] 1.27(2) [55] 0.08(1) 0.11(1)	$\begin{array}{c} 16.51(4) \\ 3.1 \\ 27.2(5) \\ 1.46(3) \\ 3.45(3) \\ 0.0008(1) \\ 0.014(2) \\ 7.7(7) \\ -0.16(2) \\ 0.046(15) \\ 59.3(8) \\ 62.04 \\ 47(0)a \end{array}$	
Kef. [58] [4f ¹³ 5d6s] ${}^{3}[3/2]_{5/2}$ [4f ¹⁴ 6p] ${}^{2}P_{1/2}$ [4f ¹³ 5d6s] ${}^{3}[3/2]_{3/2}$ [4f ¹³ 5d6s] ${}^{3}[3/2]_{1/2}$ [4f ¹⁴ 6p] ${}^{2}P_{3/2}$ [4f ¹⁴ 7p] ${}^{2}P_{3/2}$ [4f ¹⁴ 7p] ${}^{2}P_{3/2}$ [4f ¹⁴ 5f] ${}^{2}F_{5/2}$ [4f ¹⁴ 6f] ${}^{2}F_{5/2}$ [4f ¹⁴ 7f] ${}^{2}F_{5/2}$ [4f ¹⁴ 8f] ${}^{2}F_{5/2}$	$2632.81 \\ 2438.42 \\ 1724.98 \\ 1345.63 \\ 935.19 \\ 245.42 \\ 234.56 \\ 210.34 \\ 173.92 \\ 158.89 \\ 151.02$	$\begin{array}{l} \textbf{0.00075} \ [55] \\ \textbf{2.97(4)} \ [20, 53] \\ \textbf{0.27} \ [54] \\ 1.31(2) \\ \textbf{0.62(1)} [55, 56] \\ 0.14(2) \\ 0.014(5) \\ 2.43(4) \\ 1.47(2) \\ 0.93(1) \\ 0.35(1) \end{array}$	$\begin{array}{r} 47(9)^{a}\\ 0.0\\ 79(2)\\ 0.46\\ 8.45(26)\\ 1.31(4)\\ 0.018(5)\\ 0.0002(1)\\ 4.54(15)\\ 1.38(4)\\ 0.503(11)\\ 0.068(4)\\ 7.7(7)\\ -0.43(3)\\ 4(2)\end{array}$	$\begin{array}{c} 0.0\\ -79(2)\\ 0.37\\ 6.76(21)\\ -1.31(4)\\ -0.018(5)\\ 0.0001(1)\\ -0.91(3)\\ -0.275(7)\\ -0.101(2)\\ -0.014(1)\\ 0.0\\ -0.08(1)\\ -0.83(25)\end{array}$
Total Ref. [58] Ref. [59]			107(3) $90(17)^a$	-75(2) -82(13)

^aNote: Estimated by combining measured differential scalar polarizability $\alpha_{6s}^{(0)}{}_{2S_{1/2}}^{2}$ - $\alpha_{5d}^{(0)}{}_{2D_{3/2}}^{2}$ from Ref. [59] and E1 matrix elements from the measured lifetimes of many states as listed in

Ref. [58].

DHF value is found to be larger than the RPA value, the CCSD value is larger than the other two methods. Though RPA is also an all order method, but it takes into account only the core-polarization correlations while the CCSD method includes pair-correlation effects to all orders along with the core polarization effects. We, therefore, consider CCSD value as the final result and it is given along with the estimated uncertainty in Table 2.1.

The "Tail" contributions to $\alpha_{J_n}^{(i,v)}$ and $\alpha_{J_n}^{(i,cv)}$ are also given in Table 2.1, which are obtained using the DHF method. The core-valence contributions are found to be negligibly small, whereas the "Tail" contributions to the scalar and tensor polarizabilities of the $[4f^{14}5d]^2D_{3/2}$ state are significant. Accounting all these contributions, we obtain the final static polarizability of the ground state as 59.3(8) a.u., while the static scalar and tensor polarizabilities of the $[4f^{14}5d]$ ²D_{3/2} state are 107(3) a.u. and -75(2) a.u., respectively. An earlier calculation using the relativistic many-body perturbation theory (MBPT) presents ground state static polarizability value as 62.04 a.u.[48]. The discrepancy arises from the value of E1 matrix elements that are used for the transitions involving the $[4f^{14}6p] {}^{2}P_{1/2}$ and $[4f^{14}6p] {}^{2}P_{3/2}$ states. On the other hand, Lea et. al. have also estimated this value as 47(9) a.u. [58] using experimental E1 matrix elements. In their calculation, only few E1 transitions were included and the core contribution was completely neglected. Nevertheless, both the values agree within their respective uncertainties. Accurate calculations of $\alpha_{J_n}^{(i)}$ for the [4f¹⁴5d] ²D_{3/2} state is very challenging as compared to the ground state. Here, the E1 matrix elements between this state and the $[4f^{14}]$ np] ${}^{2}P_{1/2,3/2}$ and $[4f^{14} nf] {}^{2}F_{5/2}$ states, with the principle quantum number n, mainly contribute. We observe from Table 2.1 that contributions from the E1 matrix elements involving the $[4f^{14} nf] {}^{2}F_{5/2}$ states converge very slowly as compared to the $[4f^{14} np] {}^{2}P_{1/2,3/2}$ states with higher n value. We, therefore, consider n = 5, 6, 7, 8 for the $[4f^{14} nf]^2 F_{5/2}$ states and n = 6, 7for the [4f¹⁴ np] ${}^{2}P_{1/2,3/2}$ states in the evaluation of "Main" contribution to $\alpha_{J_n}^{(i,v)}$. Among these contributions, the E1 matrix element of the $[4f^{14}5d] {}^{2}D_{3/2} \rightarrow [4f^{14}6p] {}^{2}P_{1/2}$ transition dominates. The contribution from the $[4f^{14}5d] {}^{2}D_{3/2} \rightarrow [4f^{14}6p] {}^{2}P_{3/2}$ transition is found to be about ten times smaller than the above transition. This is owing to the fact that its E1 matrix element is almost half while its transition energy is twice larger than the corresponding value of the [4f¹⁴5d] ${}^{2}D_{3/2} \rightarrow [4f^{14}6p] {}^{2}P_{1/2}$ transition. Noticeably contributions from the [4f¹³5d6s] configurations are non-significant as seen in Table 2.1. Unlike for the ground state, the "Tail" contribution to $\alpha_{L_n}^{(0)}(0)$ is about 4% of its total value. Thus, the dominant sources of uncertainty in $\alpha_{J_n}^{(o)}$ comes from the "Tail" contribution. Similar trends are also observed in the determination of the static $\alpha_{J_n}^{(2)}$ value of the [4f¹⁴5d] ²D_{3/2} state, which are also quoted in Table 2.1. Lea et. al. have estimated its scalar polarizability value as 90(17) a.u. [58] considering only few E1 matrix elements deducing from the experimental lifetime values. The reason for which we believe to obtain more reliable results for α of the ground and $[4f^{14}5d] {}^{2}D_{3/2}$ than the previously estimated values is because of use of more accurate values of the E1 matrix elements of



the [4f¹⁴5d] ${}^{2}D_{3/2} \rightarrow [4f^{14}6p] {}^{2}P_{3/2}$, [4f¹⁴5d] ${}^{2}D_{3/2} \rightarrow [4f^{14} 5f] {}^{2}F_{5/2}$ and [4f¹⁴5d] ${}^{2}D_{3/2} \rightarrow [4f^{14} 6f] {}^{2}F_{5/2}$ transitions, and core contribution that are evaluated using our RCC method. An experimental value of the tensor polarizability of the [4f¹⁴5d] ${}^{2}D_{3/2}$ state has been reported as -82(13) a.u. [59]. From the comparison, we find our estimated value agrees with the experimental result indicating that we are able to produce polarizabilities in the investigated states accurately.

Using the static polarizability values as mentioned above, we estimate the BBR shifts in the ground and $[4f^{14}5d] {}^{2}D_{3/2}$ states of Yb⁺ as -0.52(1) Hz and -0.92(2) Hz, respectively. Further, we evaluate the dynamic corrections to these quantities approximately, using Eq. (2.11), as -0.0004(1) Hz and -0.04(1) Hz in the ground and $[4f^{14}5d] {}^{2}D_{3/2}$ states respectively. From this analysis, we obtain the net differential scalar polarizability value of the $[4f^{14}6s] {}^{2}S_{1/2} \rightarrow [4f^{14}5d] {}^{2}D_{3/2}$ transition as $-7.8(5) \times 10^{-40} \text{ Jm}^2 \text{V}^{-2}$ against the available experimental value of $-6.9(1.4) \times 10^{-40} \text{ Jm}^2 \text{V}^{-2}$ [59]. This corresponds to the differential BBR shift for the $[4f^{14}6s] {}^{2}S_{1/2} \rightarrow [4f^{14}5d] {}^{2}D_{3/2} = 10.37(5) \text{ Hz by Tamm et al } [60]$, which is being currently considered for estimating uncertainty due to the BBR shift in the above clock transition frequency. There are also two different values predicted as -0.254(5) Hz and -0.261(8) Hz by Lea et al obtained from the *ab initio* calculations and by combining both the experimental and theoretical data, respectively [58].

2.4.2 Magic wavelengths for linearly and circularly polarized light

In order to find out λ_{magic} among different magnetic sublevels M_F of the $[4f^{14}6s] {}^2S_{1/2}(F = 0) \rightarrow [4f^{14}5d] {}^2D_{3/2}(F = 2)$ clock transition we estimate dynamic α_{F_n} values of the associated states at different wavelengths ranging from 200-2000 nm. First the atomic polarizabilities of the $[4f^{14}6s] {}^2S_{1/2}$ and $[4f^{14}5d] {}^2D_{3/2}$ states are determined using the relations given in Eqs. (2.7-2.9), then total polarizability value is obtained from Eq. (2.3). In Fig. 2.2 we plot our estimated α_{F_n} values of both the states as function of the wavelength. To obtain the λ_{magic} values for the $|4f^{14}6s {}^2S_{1/2}, F = 0\rangle \rightarrow |4f^{14}5d {}^2D_{3/2}, F = 2\rangle$ transition, we have considered all possible combinations of M_F sublevels and also different light polarizations. In Fig. 2.2(a), we plot the dynamic polarizabilities for the states associated with the $|^2S_{1/2}, F = 0\rangle \rightarrow |^2D_{3/2}, F = 2, M_F = 0, \pm 1, \pm 2\rangle$ transitions against the wavelength corresponding to a linearly polarized light. Similar plots are shown in Fig. 2.2(b) for the $|^2S_{1/2}, F = 0\rangle$ and $|^2D_{3/2}, F = 2, M_F = -2, -1, 0, 1, 2\rangle$ states assuming a circularly polarized light. The wavelengths at which intersections of the polarizability curves take place are recognized as λ_{magic} . In Table 2.2 we list the λ_{magic} values for the $|4f^{14}6s {}^2S_{1/2}, F = 0\rangle \rightarrow |4f^{14}5d {}^2D_{3/2}, F = 2\rangle$ transition in the presence of a linearly polarized light. It can be noticed that these λ_{magic} values

	Linear			Circular	
M_F	$\lambda_{ m magic}$	α_{F_n}	M_F	$\lambda_{ m magic}$	α_{F_n}
0	1725.4(1)	62(2)	-2	1726.5(1)	60(5)
	1356.3(3)	63(2)		1381.8(6)	62(2)
	951.8(5)	66(2)		948.9(2)	64(2)
	357.3(1)	21.03(1)		358.1(2)	-6.2(3)
	344.9(1)	21.81(4)		345.2(2)	-5.1(3)
	298.8(3)	25.68(3)		299.1(1)	-1.31(6)
	291.8(1)	26.52(6)		292.2(1)	-0.80(1)
	245.4(1)	-53.43(3)		243.5(1)	-55.3(2)
± 1	1726.6(1)	61(2)	-1	1728.8(3)	61(2)
	1382(1)	63(2)		1418(2)	62(2)
	948(1)	65(2)		944.4(2)	67(3)
	357.3(2)	20.61(3)		357.8(1)	5.7(1)
	344.9(1)	21.01(2)		345.1(1)	6.4(2)
	298.8(1)	24.62(6)		299.0(1)	9.41(5)
	291.8(1)	25.43(5)		292.1(1)	9.89(7)
	245.4(1)	-53.59(6)	0	1735.4(2)	61.4(7)
± 2	1788(6)	63(1)		1479(5)	62(5)
	1619(5)	59(1)		939.7(2)	67(2)
	357.4(2)	18.0(2)		357.4(2)	18.8(2)
	345.1(2)	18.42(2)		345.0(2)	19.3(3)
	298.8(1)	20.63(5)		298.8(1)	21.96(5)
	291.9(1)	21.20(5)		291.9(1)	22.50(7)
			1	1657(15)	61(2)
				356.9(2)	32.1(6)
				344.8(2)	32.5(3)
				298.7(1)	35.30(5)
				291.7(1)	35.84(5)
			2	1204(45)	64(2)
				356.5(2)	46.6(9)
				344.7(2)	47.0(5)
				298.5(1)	50.31(7)
				291.6(1)	51.17(9)

Tab. 2.2: The λ_{magic} values in nm and their corresponding α_{F_n} values in a.u. values among different M_{F_n} sub-levels of the [4f¹⁴6s] ${}^2S_{1/2}(F = 0) \rightarrow [4f^{14}5d] {}^2D_{3/2}(F = 2)$ clock transition in ${}^{171}\text{Yb}^+$ are listed for both the linearly and right-circularly polarized light.

support a red-detuned trap (depicted by the positive values of dipole polarizabilities at these wavelengths) except at 245.4 nm. Magic wavelengths at the infrared region are considered to have more experimental significance due to availability of high power fiber or solid state lasers in comparison to ultraviolet wavelengths produced by the sum or difference frequency generation techniques. Similarly, the λ_{magic} for the $|4f^{14}6s \ ^2S_{1/2}, F = 0\rangle \rightarrow |4f^{14}5d \ ^2D_{3/2}, F = 2\rangle$ transition in the presence of a right-circularly polarized light are also given in Table 2.2. In

$[4f^{14}6s]^2$	$\overline{\mathrm{S}_{1/2}(F)}$	7 = 0)	$[4f^{14}5d]^{2}$	$^{2}\mathrm{D}_{3/2}(I)$	7 = 2)			
Polarization	M_F	λ_T	Polarization	M_F	λ_T	Polarization	M_F	λ_T
Both	0	357.9(1)	Linear	0	1725.4(1)	Circular	-2	1726.9(1)
		345.2(1)			1370.2(3)			1398.1(2)
		299.1(1)			1026.9(8)			961.7(1)
		292.2(1)			741.2(5)			283.3(4)
				± 1	1727.8(1)		-1	1731.3(1)
					1438.6(3)			1478.1(1)
					1021(1)			961.6(1)
					774(4)			468.2(7)
				± 2	1712.4(3)		0	1826(2)
					966(4)			1688.3(3)
								1000(2)
								861(3)
							1	1719.4(1)
								1233(1)
							2	-

Tab. 2.3: The $\lambda_{\rm T}$ values in nm for all possible M_F components of the considered states in ¹⁷¹Yb⁺ using linearly and right-circularly polarized light. The term "Both" for [4f¹⁴6s] ${}^{2}S_{1/2}(F = 0)$ state means that the results are same for linear and circular polarization.

the present work, we determine λ_{magic} for right circularly polarization using A = 1 considering all possible positive and negative M_F sublevels of the states participating in the transition. Note that λ_{magic} for the left circularly polarized light of a transition with a given M_F are equal to right circularly polarized light with opposite sign of M_F .

2.4.3 Magic wavelengths for comprehensive trapping

In Eqn.2.3, we observe that the polarizability of the state is independent of M_F sublevels if we make tensor and vector component of polarizabilities equal to zero. The vector polarizability of the state goes to zero, if we use laser light with linear polarization and the tensor polarizability goes to zero, if $\cos^2\theta_P$ values is 1/3. To reduce or cancel the tensor component, we change our system configuration in such a way that, the angle between quantization axis of system and the polarization axis of light is nearly 54⁰. In this way we reduce the tensor part, which makes the trapping of ion independent of M_F values. In Fig. 2.3, we show λ_{magic} values that are independent of the hyperfine sub-levels and their M_F sublevels of the states involved in the clock transition. These λ_{magic} values will produce null differential Stark shifts exclusively among any hyperfine level of the ground and excited state. We have identified at least seven magic wavelengths for the hyperfine level independent trapping, out of which four are in the ultra violet and two in the infra red region. The λ_{magic} values along with the polarizability values at these wavelengths are marked with arrows in Fig. 2.3. In order to distinguish the M_F



Fig. 2.3: Dynamic scalar polarizabilities of the $[4f^{14}6s] {}^{2}S_{1/2}$ and $[4f^{14}6s] {}^{2}D_{3/2}$ states when vector and tensor polarizability components are zero. indicated within brackets. The vertical dashed lines signify the resonances from the ground and $[4f^{14}5d]^2D_{3/2}$ states in green and purple independent of the hyperfine levels and magnetic sub-levels of the states. In particular to the λ_{magic} , their corresponding α_F values are colors, respectively. The λ_{magic} and λ_{T} wavelengths for comprehensive trapping are pointed out in dots and encircled by circles and squares, respectively, are

Parameters	Experiment [59]	Our calculation
$\alpha_{\rm s}(^2{ m S}_{1/2})$		$9.7(1) \times 10^{-40} \text{ Jm}^2/\text{V}$
$lpha_{ m s}(^2{ m D}_{3/2})$		$17.6(5) \times 10^{-40} \text{ Jm}^2/\text{V}$
$\alpha_{\rm T}(^2{\rm D}_{3/2})$	$-13.6(2.2) \times 10^{-40} \text{ Jm}^2/\text{V}$	$-12.3(3) \times 10^{-40} \text{ Jm}^2/\text{V}$
$\Delta \alpha_s = {}^2\mathrm{S}_{1/2} - {}^2\mathrm{D}_{3/2}$	$-6.9(1.4) \times 10^{-40} \text{ Jm}^2/\text{V}$	$-7.8(5) \times 10^{-40} \text{ Jm}^2/\text{V}$
BBR shift ${}^{2}S_{1/2}$		-0.52(1) Hz
BBR shift ${}^{2}D_{3/2}$		-0.92(2) Hz
Dynamic correction $^2\mathrm{S}_{1/2}$		-0.0004(1) Hz
Dynamic correction $^{2}D_{3/2}$		-0.04(1) Hz
Total BBR shift ${}^{2}D_{3/2}$ - ${}^{2}S_{1/2}$	-0.37(5) Hz	-0.44(3) Hz

Tab. 2.4: Summarized values for BBR and polarizabilities of the clock states

independent magic wavelength, the λ_{magic} presented in Table 2.2 for linearly and circularly polarized light can be compared to the values marked in Fig. 2.3. The trapping scheme as proposed in Ref. [44] and using λ_{magic} indicated in Fig. 2.3, one can optically trap Yb⁺ with zero light shift in the [4f¹⁴6s] ${}^{2}S_{1/2} \rightarrow [4f^{14}5d] {}^{2}D_{3/2}$ transition for any given combinations of the *F* and M_{F} levels that are experimentally viable.

2.4.4 Tune-out wavelengths

In many experiments, there is a requirement of trapping atom/ion in optical lattice without affecting any one of the energy states. In this type of experiment, tune-out wavelength is used. At tune out wavelength, the polarizability of the state is completely independent of intensity of light used for trapping which makes our desired state unperturbed. In our calculation, we identified polarization dependent tune-out wavelengths at which the dynamic polarizabilities reduces to zero individually with different M_F sub-levels in both F = 0 state of $[4f^{14}6s] {}^{2}S_{1/2}$ and F = 2 state of $[4f^{14}5d] {}^{2}D_{3/2}$. These $\lambda_{\rm T}$ values are listed in Table 2.3 together with the M_F values against wavelengths of light with linear and circular polarizations. We have also identified four tune-out wavelengths for the $[4f^{14}5d] {}^{2}D_{3/2}$ state at 1734.2(2), 1533.2(1), 993.5(5), and 753(3) nm, which could be useful for carrying out measurements irrespective of choice for hyperfine sub-levels, as given in Table 2.3

2.5. Conclusion

We have calculated polarizability of ground state [4f¹⁴6s] ${}^{2}S_{1/2}$ and metastable [4f¹⁴5d] ${}^{2}D_{3/2}$ states of Yb⁺. In this calculation, we have used few theoretical techniques like DHF, MBPT, RPA CCSD, RCC and CI and experimental data like branching ratio and life time of the excited state, for estimating the magnitude of E1 transition matrix, which are contributing in polarization. The polarizability of states is used for estimation of systematic shifts like Stark shifts and BBR shifts for clock transition $6S_{1/2} \rightarrow 5D_{3/2}$ of Yb ion. The summarized values for our calculation and compared with the experimental values are tabulated in Tab. 3.6. We have also calculated the dynamic polarizability for wavelength range from 200 nm to 2000 nm, which gives the values of magic wavelengths and tune out wavelengths for the clock states. In these calculations, we also searched for magic and tune out wavelength for comprehensive trapping, where, in this type of trapping, the polarizability is independent of magnetic quantum number of the atomic state. After all these calculations, we found that there are possibilities of trapping ion in optical lattice and the lattice laser wavelength is equal to one of the magic wavelength, which gives zero Stark shift during clock transition. Our results will be of interest for carrying out precision measurements in the [4f¹⁴6s] ${}^{2}S_{1/2} \rightarrow [4f^{14}5d] {}^{2}D_{3/2}$ transition of Yb⁺ for attaining high accuracy clock frequency and investigating many fundamental physics.

Chapter 3

Low Noise Computer Controlled DC Voltage Source

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3.1. Introduction

In every precision experiment a series of processes are being carried out following a prescribed sequence. The outcome of which depends on execution of a particular protocol and synchronous operation of the modules associated to it. Manual control restricts the achievable accuracy due to human errors introduced in the protocol and its shot-to-shot variation which also disturbs the repeatability and reproducibility of the data. Due to this reason, automation is required which minimizes the manual errors such as, input errors, delays in simultaneous operation, shotto-shot variations and so on. Thus, state-of-the-art experiments as the one going on in our laboratory for building optical frequency standard using single trapped Ytterbium-ion [61, 62] needs to be automated so that it can operate uninterruptedly over a period of weeks. This also helps in self-diagnosis of any malfunctioning in modules associated to the experimental system. To give a figure of merit, in our experiment we will use five voltage sources for trapping ions [63, 64]; three current sources for producing magnetic fields; five lasers for photoionization, laser cooling and probing the clock transition; twenty optical modulators for generating desired laser frequencies [65]; ten servo controls for frequency and power stabilization of the laser beams [66, 67]; twenty mechanical shutters for blocking-unblocking laser beams [68] and so on as shown in Fig. 3.1. Apart from the large number of control knobs, operation of all the devices are time dependent and needs to be synchronized with each other in a single experimental run. This will be repeated over few days in order to achieve better statistics.

Worldwide many labs have indigenously developed hardware and software to incorporate automation, which are required for sequencing, interfacing and controlling their experiments remotely, which are given in the references [70, 71, 72, 73, 74, 75, 76]. As per the requirement in our experiment, we have also developed a digitally operated addressing and control module (DACM) which is capable of automating our indigenously developed modules. The module is fabricated using off-the-shelf parts and the design is simple to construct, as described in this chapter.



Fig. 3.1: A schematic of our experiment which shows the requirement of automation [69].

We have also developed a multipurpose computer programmable, low noise voltage source and verified its operation by controlling our DACM. The use of DACMs are universal but not limited to operate the voltage sources. We use these to computer control a wide variety of instruments in our lab, such as current source, AOM drivers, EOM drivers, and so on. Some of their other applications, such as AOM/EOM drivers using DACM is described in Chapter 4. Detailed description and characteristics study of this voltage source is given in the following sections. Since this voltage source is programmable, it is also used as an arbitrary waveform generator for low frequency operations. Such computer controlled and stable high voltage sources have many applications at low frequencies. As for example, our voltage source can be used to drive the piezo in an extended cavity diode laser (ECDL) for smooth scanning or ramping of the laser frequency from a computer or tuning length of a Fabry-Perot cavity.



Fig. 3.2: (a)Block diagram of discrete transistor shunt voltage regulator use for design a low noise computer control regulated voltage source.(b) Front view of the voltage source and its internal circuit assemblies

Of particular interest, we have developed this voltage source for biasing the electrodes of our ion-trap [61, 63].

3.2. Theory

Automation is one of the main requirements in any kind of sophisticated experiments as it improves the repeatability and reproducibility of the data of the measurement. In our experiment, we have many different modules which are connected through a common digital bus. Remote control of all modules through common digital bus serially requires a communication protocol. In our experiment, we use a new type of protocol which consists of three stages: trigger, decoded address and data transfer. In this protocol, pulse width of the trigger is decided by the time for the events to occur *i.e* it decides the speed of the communication. In our system, there are many modules, which are used simultaneously to perform experiment. When we impart address to any module from the computer in terms of digital bits, the same has to be decoded at the other end for which we use decoders. The decoders have one active high pin and other active low corresponding to input digital bits and its functional equation is

$$O_n = X_0 \times D_i \tag{3.1}$$

where O_n is n^{th} output pin, X_o is condition for decoding and D_i is input digital number. When we give the correct condition to the decoder and send a digital number D with corresponding binary number i, then i^{th} number of pin of output is active high. Using the active High pin, we setup the following logic for data transfer from computer to module:

$$O_{cn} = O_n \times I_{cn} \tag{3.2}$$

where O_{cn} and I_{cn} are output and input control bits used for controlling modules used in the experiment. The used protocol is faster than the conventional serial communication because all bits are transferred parallelly using data bus; only trigger pulse goes serially.

Other than automation module, we have also developed a low noise computer controlled regulated voltage source, which is used for controlling ion motion inside the trap. The regulated low noise voltage source has four main parts: AC to DC convertor, reference source, feedback amplifier and control element. In AC-DC convertor, we use bridge rectifier with RC type filter which is able to produce an unregulated DC voltage with small ripples of the form,

$$\gamma = \sqrt{\frac{V_{rms}^2}{V_{DC}}^2 - 1} \tag{3.3}$$

where γ is ripple factor, V_{rms} and V_{DC} is input AC and output DC voltage, respectively. As per our requirement of voltage source in our experiment, the ripple factor is very high, so we need an additional low noise DC voltage circuit. In our case, we use a discrete transistor shunt voltage regulator which is used to further minimize the ripples and make a regulated, low noise voltage source. The corresponding block diagram is shown in Fig. 3.2. For feedback amplifier, we use Opamp for comparing the reference and unregulated voltage which gives feedback to control element, so that the final output is a regulated voltage. In this kind of circuit, the output noise mainly depends upon the reference source which is responsible for the stability and uncertainty of the output voltage. As shown in Fig. 3.2, total current drawn from the unregulated source is

$$I_{total} = I_1 + I_2 + I_3 \tag{3.4}$$

Tab. 3.1: Truth table of DEC-1 when $\{\overline{\mathbf{E}}_{i1}; \overline{\mathbf{E}}_{i2}; \overline{\mathbf{E}}_{i3}\}$ operates at $\{0; 0; 1\}$. The' \rightarrow ' indicates operating state of DEC-1 as shown in Fig. 3.3 (a) which can further generate 8 different addresses after DEC-2.[77]

	Inp	out		Output							
	I_1	I_2	I_3	\overline{o}_{17}	\overline{o}_{16}	\overline{o}_{15}	\overline{o}_{14}	\overline{o}_{13}	\overline{o}_{12}	\overline{o}_{11}	\overline{o}_{10}
	0	0	0	1	1	1	1	1	1	1	0
	0	0	1	1	1	1	1	1	1	0	1
	0	1	0	1	1	1	1	1	0	1	1
	0	1	1	1	1	1	1	0	1	1	1
	1	0	0	1	1	1	0	1	1	1	1
	1	0	1	1	1	0	1	1	1	1	1
\rightarrow	1	1	0	1	0	1	1	1	1	1	1
	1	1	1	0	1	1	1	1	1	1	1

Here, we are designing a voltage source with no output current requirement, so we assume $I_3 = 0$; the feedback voltage connected to control element is

$$V_F = A(V_1 - V_2) - I_B R_B (3.5)$$

where, V_F is feedback voltage, A is opamp open loop gain, I_B is current from opamp to control element and V_1 is a fraction of output voltage (V_{out}/x), so final equation is

$$V_{out} = x \left[V_2 + \frac{(V_F + I_B R_B)}{A} \right]$$
(3.6)

where 1/x is fraction of output voltage taken from sample circuit. It is clear from this equation that the output voltage depends upon reference voltage when the open loop gain of the opamp is high. It also shows that designing a linear low noise voltage source requires low noise, linear regulated reference source with an opamp which has low noise, low offset voltage.

3.3. Circuit Design

This section gives a complete description of our voltage source that includes the DACM that provides unique address to module for its control through a computer and complete description of the hardware of the DC voltage source. Controlling the DC voltage source through computer using common data bus requires a unique address to each module. Due to this requirement, we have indigenously developed DACM, that interfaces to the equipment with the PC by using a commercially available data acquisition card which is programmed in the Laboratory Virtual Instrument Engineering Workbench(LabVIEW) platform. This module is designed in such a way that it can control 64 devices, which can be multiplexed further if necessary. The DACM circuit consists of a dual re-triggerable monoshot (Mono)74LS123, a dual input quad NAND gates(N) 7400, and a pair of three input-eight output decoder (DEC)74LS138, as shown in



Fig. 3.3: Circuit diagrams for: (a) generating unique individual address for an equipment which is required for reading them from a PC, (b) DACM for writing data to the particular equipment, and (c) constant dc voltage source. [77]



Fig. 3.4: (a) Schematic of DACM which is designed in Eagle v6.4 and (b) Populated PCB board.

Fig. 3.3(a). This module gets activated upon receiving of a trigger pulse (in the form of TTL) which by default comes from the data acquisition (DAQ) card or can also be received from an external precise pulse generator such as atomic clock. We design this module in such a way that it triggers the process of generating desired address and transfers control data only at the rising edge of the trigger pulse. At rising edge and falling edge, mono 1 and mono 2 are activated one by one, respectively for a short interval of time. The pulse width of the Monos is set to 2.8 μ s, which is very less than the pulse width of trigger pulse t_w . The complement output of the Monos are connected to the two input of RS-flip flop, that is constructed using N-1 and 2 of a quad NAND gate and is used as a state shifter. These flip flops change their



Fig. 3.5: Schematic of power supply section used to design low noise voltage source.

states only at the rising and falling edges if the pulse width of clock is larger than pulse generated by the Monos. In this RS-flip flop, there is invalid condition when all inputs are in low state. To avoid this condition, we connect the input of RS-flip flop with complement of the monostable multivibrators. The output of flip flop is then connected to two decoders in such a way that it only enables decoders when trigger pulse is active high as shown in Fig. 3.3(a). The decoder DEC-i (for i=1 and 2) decodes three inputs-to-eight outputs $o_{i0}; \ldots, o_{i7}$ based on the binary inputs I_1 ; I_2 ; I_3 and the conditions applied on the three enabling pins \bar{E}_{i1} ; \bar{E}_{i2} ; \overline{E}_{i3} . The DEC enables only when \overline{E}_{i1} ; \overline{E}_{i2} ; \overline{E}_{i3} operates at 0; 0; 1. For that, outputs of the RS-flip flop are used for enabling \bar{E}_{i2} , \bar{E}_{i3} of both the DECs and \bar{E}_{11} is kept at low. Since the outputs of the DEC are inverted, only the decoded address bit stays at low, following the truth table as given in Tab.3.1. In this configuration, DEC-1 can assign eight addresses only. We use a pair of DECs in such a way that when a particular output \bar{o}_{1n} (for any n = 0 to 7) of DEC-1 is at low, it gets connected to \bar{E}_{21} of the DEC-2 for multiplexing the addressing output to sixty four. The schematic of the DACM is also shown in Fig. 3.4. In order to control the output, a reference voltage is given in a 16-bit digital format r_0 ;... r_{15} to a pair of eight input-and-eight output Latches, SN74HC373, which are connected in parallel within the DACM as shown in Fig. 1 (b). The particular address bit \bar{o}_{2n} , which is at low, is inverted using a NOT-gate and connected to LE of Latch-1 and 2. The latches become transparent only when their output control (OC) and latch enabled LE terminals are 0 and 1, respectively. The duration over which the latch remains enabled depends on the TTL pulse width. Here, since



Fig. 3.6: (a) Schematic of voltage source which is designed in EAGLE V6.4 software and (b) populated PCB board.

OC are grounded, outputs $\overline{D}_0, \ldots, \overline{D}_{15}$ of the Latch-1 and 2 follow their reference inputs in real time only when LE becomes high. Once the data bits pass through the enabled latches, an analog reference voltage is generated corresponding to r_0, \ldots, r_{15} by a 16-bit digital-to-analog converter, DAC712. For giving a different reference voltage, a new set of $r_0; \ldots, r_{15}$ is sent from PC along with its address. The data bits reach to all DACMs that are attached to the data acquisition system but the ones that are addressed can only read it. This addressing module is versatile for all those equipment that require an analog electronic signal for controlling. The DACM allows a significant reduction in a lab's cable infrastructure: instead of point-to-point connections with individual cables, all equipment may be connected via a single digital bus that runs throughout the lab.



Fig. 3.7: Behavior of a digital BUS (black; red; green; blue) which is propagated through different strings of a 30 m long cable. Digital bits are at Low between 0 to 0.8 V and high between 2.5 to 5 V. At the end of 30 m cable the time dependent BUS are 0; 0; 0; 0 at t = t0; false; 0; 0; 0 at t = t1; 1; 1; 1; 0 at t = t2 and the desired 1; 1; 1; 1 at t \geq tD.[77]

For the DC voltage source, the 220 V, 50 Hz AC is first converted to an unregulated DC voltage, $V_{dc} = 120$ V using combination of a step down transformer, bridge-wave rectifier and a filter. Further regulation and rectification of the ripples is obtained by using a discrete transistor shunt voltage regulator type configuration, as shown in Fig. 3.3(c). This unregulated DC consists of AC ripples which are typically on the order of few 100 mV. In this circuit, tuning of the output is done by varying the reference voltage rather than by varying any active component in it. The analog reference voltage V_{ref} which is generated by a 16 bit DAC-712 is compared with the rectified DC and the final output is self-corrected with the help of a transistor which is used as a control element. Output of the rectifier V_{dc} is current limited to 21 mA using a series resistance R_s with it. Since the range of V_{ref} is limited to 0-10 V, the comparator is established for the full range of V_{dc} by stepping it down to a factor. This scaling is done by using R1 and R2 in a voltage divider configuration which is like a sampling circuit. The $V_{in} = V_{dc}/10$ is compared with V_{ref} at the non-inverting and inverting terminals of an OPAMP and OP-07, respectively. Output of the OPAMP which is proportional to V_{in} - V_{ref} , is applied onto the base B of an NPN-transistor, BU-508, in common emitter configuration. A fraction of the maximum allowed current, V_{dc}/R_s gets diverted through the shunt voltage regulator which is proportional to V_{in} - V_{ref} . If $V_{in} > V_{ref}$, the current passing through the transistor increases which results in decrease of V_o since V_{dc}/R_s is constant, or vice-versa.

S.No	Items	Quantity
1.	LT1175	2
2.	LT1763	3
3.	Step down transformer	1
4.	Rectifier	5
5.	NPN transistor BU508	1
6.	OP07	2
7.	BU508	1
8.	16 bit DAC-712	1
9.	Monostable multivibrator 74LS123	1
10.	3 to 8 Decoder $74LS138$	2
11.	Quad NAND Gate 7400	1
12.	8 bit Latch 74LS373N	2
13.	Right angle BNC PCB mount	2
14.	DB-25	1 pair
15.	8 pin header PCB mount	1
16.	18 pin header PCB mount	3
17.	Dual 8 pin header PCB mount	2
18.	Jumper	2
19.	2 pin header PCB mount	3
20.	6 pin header PCB mount	2
21.	Digital voltmeter	1
22.	3 pin power panel	1
23.	18 pin flat cable 0.5 m	2
24.	8 pin flat cable	1
25.	2 pin flat cable	2
26.	6 pin flat cable	1
27.	100k POT PCB mount	2
28.	10k resistance	5
29.	100k resistance	2
30.	90 k resistance	1
31.	1 k resistance	4
32.	500 resistance	2
33.	2 k resistance	2
34.	6k resistance	2
35.	150k resistance	2
36.	442k resistance	1
37.	50k resistance	1
38.	100 nF capacitor	50
39.	10 nf	10
40.	$1\mu \mathrm{F}$	4
41.	$10\mu F$	2
42.	Power cable	1
43.	Toggle switch	1
44.	50 pin flat ribbon cable	1
45.	NI DAQ card with 50 PIN Digital I/O	1
46.	22 μ F bypass capacitor for final output	2

Tab. 3.2: Bill of materials to construct the DACM and voltage source



Fig. 3.8: Change of voltage source output with the reference voltage which is tuned from a PC. A linear fit to the data establishes our model. We use $R_1 = 91k\Omega$, $R_2 = 10k\Omega$, $R_B = 3.9k\Omega$, $R_s = 5.7k\Omega$, $V_{dc} = 119$ V, $V_{BE} = 0.7$ V, $V_E = -4.9$ V, $\beta = 5$ and A = 450×103 . [77]

The emitter, E, of the transistor is biased at -5 V in order to keep it active in the vicinity of output voltage, $V_o = 0$ V. A diode is connected in between B - E for protecting the comparator output current to pass through the transistor. In this way, the comparator equilibrates at V_o according to the V_{ref} within a transient time to produce an ultra-stable regulated output. For generating V_{ref} , we have used a 16-bit DAC, DAC-712, in order to achieve sub mV resolution at the output. In order to get a stable output of the DAC and hence a stable output of the voltage source, we separate the digital and analog grounds which are interconnected at one place. This resolution can be further improved by using an 18 or 32 bit DAC, but this will also require special arrangement for reducing fluctuation in the ground level. In our design we take care of the noise reduction in power supply which is connected to transistor and comparator and responsible for the system noise. In our power supply section, we first used unregulated DC voltage $\pm 18V$ and $\pm 7V$ from a bridge rectifier by using small tapping in transformer which generates 120 V from main output voltage. Then we use low noise adjustable voltage regulator LT1175 and LT1763 for negative and positive voltages for biasing the active components in the circuit. For further reduction of noise, we use LC filter in input and for output we use two type of bypass capacitors, one is polar high capacitance for low frequency noise and other is small ceramic capacitor for high frequency noise; the schematic of power supply section is shown in Fig. 3.5. Our module is simple in terms of design and uses off the shelf components in comparison to the commercially available FPGA based voltage sources, however provides similar stability and resolution. The schematic of the voltage source designed using EAGLE software, using which the PCB has been architectured is also shown in Fig. 3.6. List of components required for designing of the DACM and voltage source are given in Tab. 2.

3.4. Result and Discussion

The DACM uses a common address BUS consisting of 6 digital outputs (DOs) from DAQ card for reading a particular device, a data BUS consisting of 16 DO for writing data and a trigger pulse to start the bit transfer from DAQ to module. Fast sampling of data causes errors in output due to bit slip, which is mainly due to random propagation delay in Data BUS from DAQ to module through 50 pin flat ribbon cable. We first measured delays of four digital bits array after they propagated through 30 m long cables which are shown in Fig. 3.7. This data gives the limiting speed of our communication protocol because the trigger pulse width t_w used to provide discrete data to DACM is always greater than that of propagation delay. To ensure that no such data slips occur, we use pulse width timing 10 times more than the random delay. In this measurement, the approximated delay is $0.4(1) \ \mu$ s when using 30 m wire. If we extrapolate this result linearly, rough estimation of the speed of our DACM for delivering data to module is approximately $1.4 \ \mu/m$.

In Eq.3.6, it is clear that the output of the voltage source follows V_{ref} and their linear dependency can be established from simple analysis of the circuit as shown in Fig. 3.3 (c). Potential from feedback amplifier connected to transistor, which acts as a control element is equivalent to

$$V_{BE} + V_E = A(V_{in} - V_{ref}) - I_B R_B, (3.7)$$

where A is open loop gain of the OPAMP, V_{BE} , V_E are potentials across the base-emitter and emitter-ground, respectively, $V_{in} = V_0 \frac{R_2}{(R_1+R_2)}$ and $I_B = IR_1 - V_o + \frac{V_A}{R_1\beta}$ for collector-toemitter current ratio β of the transistor. The current $I = \frac{(V_{dc}-V_o)}{R_s}$ flowing through the circuit establishes the linear relation between V_o and V_{ref} as

$$V_o = \left[V_{ref} + \frac{V_{BE} + V_E + V_{dc} + \frac{\alpha}{\beta}}{A}\right] \left(\alpha_1 + \frac{\alpha_4}{A}\right)^{-1}$$
(3.8)

where $\alpha_1 = \frac{R_2}{(R_1 + R_2)} R^2 / (R^1 + R^2)$, $\alpha_2 = \frac{R_B}{R_s}$, $\alpha_3 = \frac{R_B}{R_1}$ and $\alpha_4 = \frac{(\alpha_2 + \alpha_3)}{\beta} - \alpha_1 \alpha_3$.

Figure 3.8 shows operation of the voltage source which follows Eq. 3.8. Linearity percentage was calculated using values from experimental output and values obtained by using Eq.3.8



Fig. 3.9: Stability of the output after starting the voltage source (black) and after five hours of continuous operation (red). (a) Distribution of the output in 2500 data points during 500 s run time. This measures deviation of V_o from a desired set voltage. (b) Magnitude of the average fluctuation at different set voltages. The band (cyan) depicts mean magnitude of all the fluctuations over the range of operation, which is 1.90(36) V.[77]

and was found to be 99.52%. We also calculated linearity correlation of our experimental input and output data and we found that these data are correlated linearly with correlation coefficient 1.00007. These voltage sources have been operated for days to investigate their short and long term stabilities. The output voltages are averaged over 500 s, immediately after switching on the sources and after 5 hours of operation as shown in Fig. 3.9. These Gaussian-shaped plots shows that the output voltage fluctuation arises due to random noise which is inherent in any module. The random noises in the figure are due to pickup noises arising during measurement. The outputs were recorded at intervals of 200 ms for a fixed set voltage V_{set} from a graphical user interface (GUI) in the PC and the corresponding $V_{ref} = V_{set}/10$ was also recorded. The measured load regulation at different V_o is shown in Fig. 3.10. The value of resistance below which the approximation $I_3 = 0$ becomes invalid is termed as "critical resistance". The critical resistance, R_c , below which the voltage source is unable to produce the desired V_o is also depicted in the same figure. We have characterized the line regulation for a variation of input AC between 200 V - 240 V. The regulation is 0.001% for $V_o = 0$ to 86 V and increases up to 35% at $V_o = 100$ V due to limited supply of V_dc . The improvement of line regulation in region near 100 V is possible by decreasing the source resistance R_s which causes large current to pass through the transistor and it gets heated which increases the inherent noise of the transistor and causes error in final output voltage. In our experiment we not require any current and also our AC voltage supply in our lab is stabilized by central UPS, which gives reason for no further improvement required in this design. Figure 3.9(a) shows an example distribution for the deviation of V_o from the desired output. Full width half maxima of these histograms depict stability of V_o . The magnitude of the average fluctuation of the voltage source is determined to be 1.90(36) mV over its operating range 0-100 V. The transient behavior of the voltage source at switching on/off for different values of V_o has been investigated, which is shown in Fig. 3.11. For acquiring this transient behavior we operated the voltage source in switching mode at different frequencies up to 10 Hz. It has been observed that during switching ON, the voltage source overshoots before stabilizing at V_o (Fig. 3.11a). The transient switching ON/OFF times are independent of the switching frequencies within the resolved accuracy of our measurement. The average transient times for different outputs, at switching frequencies up to 10 Hz are shown in Fig. 3.11(b). In this module, the transient time is improved by decreasing the value of bypass capacitors which are used in output channel and by using fast DACs. Increase in the speed of our module however increases the noise level which decreases the stability of the output but as we use this module as a DC source, such modification is not required.

3.5. Application

Such a computer controlled voltage source has many applications mainly in high voltage region. From Eq.3.8, it is clear that the maximum output voltage is dependent upon the maximum DC unregulated voltage and the fractional part of the voltage used in sampling circuit. Hence, to achieve higher output voltage, no major changes has to be brought about in the circuit. As for example, to achieve 0-1000 V regulated output, 1/100 of the output voltage from a transformer



Fig. 3.10: Characteristic load regulation of the voltage source. The critical load below which the voltage source is unable to maintain a constant output is also depicted. [77]

with 1000 V output is transferred to the feedback to get the regulated output. This kind of universal and easy updating feature in the system makes it user friendly. It can therefore be used for high voltage biasing applications, such as, Geiger-Muller (GM) counter, Photo multiplier tube (PMT), ion traps, plasma related or discharge tube based experiments *etc.* This module is also able to generate any arbitrary waveforms such as, triangular, sinusoidal, sawtooth as shown in Fig. 3.12. In this figure, amplitude of the waveforms are kept at 0-40 V in order to measure the voltages in the available oscilloscope. The response of the waveforms is limited to a bandwidth of 0-10 Hz as it is also shown in Fig. 3.11. The minimum increment of the output is 3 mV which is limited due to 16-bit DAC. This resolution can be improved further by using 18 or 32-bit DACs. Such high voltage sources have wide range of applications, such as, driving of the piezos attached to an ECDL laser or biasing electrodes of an ion trap and so on. The triangular or sawtooth voltages are useful for smooth scanning of the laser frequencies in the ECDL lasers. On the other hand, position of the ions confined in an ion trap can be adiabatically tuned using our voltage sources.

3.6. Conclusion

We have discussed about the design and working principle of DACM in this chapter, which hooks up with every indigenously developed electronic module. Using this module, we make



Fig. 3.11: (a) Characteristics of the switching on (red)/ off (black) of the voltage source corresponding to a V_{ref} which changes from zero-to-max (blue) and max-to-zero (cyan), respectively. (b) Mean transient times during switching on (red)/ off(black) at different switching frequencies of V_{ref} up to 10 Hz.[77]

our system fully computer controlled via single universal data bus, which avoids any extra mesh of wire in our laboratory for performing experiment. We have also developed a low noise DC voltage source which is used for biasing electrodes of the ion trap for controlling the motion of the trapped ion. This voltage source is able to generate 0 to 100 V with a resolution of 3 mV with a stability of $\pm 1.90(36)$ mV. The design of the voltage source permits the facility of changing its voltage range just by altering the voltage divider circuit keeping the other parts



Fig. 3.12: Example waveforms: sinusoidal (red), triangle (green), sawtooth (purple) and arbitrary (blue)generated from the DC voltage source.[77]

intact. This voltage source is also a programmable one and thus can also be used as a low frequency waveform generator. This kind of multipurpose voltage source has many applications like tuning piezos, scanning length of a Fabry-Perot cavity, biasing of Photo multiplier tube *etc.* A comparison with the commercial high voltage sources show that they have lower voltage stability as compared to ours and also long term voltage stability depend upon the output voltage of the source, which makes our system better.

Chapter 4

All Digital Universal Automatic Relockable Servo Controller: FPGA Based Lock-in Amplifier & PID Systems

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4.1. Introduction

Many experiments related to atom- photon interaction require a stable laser frequency source. Stabilization of laser frequency is done via a servo controller which locks a desired frequency setpoint with respect to a stable reference point. Stability of the locked point therefore depends upon the stability of reference point, bandwidth of the servo control and the locking technique used. According to modified Schawlow and Townes formula [78] by Melvin Lax, the linewidth of a laser is

$$\Delta \nu = \frac{\pi h f(\delta \nu_c)^2}{P_{out}},\tag{4.1}$$

where, f is central frequency of laser light, P_{out} is output power from laser and $\delta\nu_c$ is FWHM of the laser's cavity linewidth. This formula is modified for diode laser, where we add a factor $(1 + \alpha^2)$ to this eq. 4.1, where α is linewidth enhancement factor of the gain medium. Equation 4.1 is based on the assumption that spontaneous emissions into laser mode introduce noise amplitude during each round trip which causes change in amplitude and phase. Due to the phase fluctuation in system and absence of restoring force, phase undergo random walk and lead to phase noise power spectral density and causes linewidth. However, while performing experiments, additional noise sources such as instability in cavity length, electrical noise, *etc.* increase the line width of laser.



Fig. 4.1: (a) Block diagram of the instrument showing constituent of the external interfacing hardware, lock-in amplifier, PID and function generator, (b) schematic of the interfacing hardware.

Our experiment requires External cavity diode lasers (ECDL) at precise frequencies for different purposes. ECDLs have a spectral linewidth of few hundreds of kHz and therefore require a frequency stabilizer to lessen the instabilities of its external cavity length and diode laser current noise. Change in cavity length (optical length) due to environmental changes can be compensated by feeding the required feedback voltage to the piezo driver of the crystal attached to the cavity mirrors, using a servo controller. The same can also be employed for suppression of current noise by feeding the feedback signal to laser's current driver which is responsible for providing current to laser diode, thus stabilizing fast frequency fluctuations and reducing the laser linewidth. Several techniques and modules for servo controller for stabilizing the laser frequency are discussed in Refs. [79, 80, 81, 82]. Among these techniques, we use Pound Drever Hall technique, which encompasses different modules like function generator, lock-in amplifier, Proportional-Integral-Derivative (PID) controller, photodiode amplifier, filters, etc. In our experiment, we have five different lasers and three Fabry Perot cavities to be stabilized using this technique. To lock them, we require eight lock-in amplifiers, eight photodiode amplifiers, sixteen function generators and sixteen PID controllers in total. These components will introduce electrical noise during the signal transmission from one module to another, enhance space problem and mesh of wires around the modules in the lab. To avoid these issues, we use Field Programmable Gate Array (FPGA) so that all the said components can be programmed on a single chip, thus giving a compact and cost-effective solution.

Field-Programmable Gate Array [83, 84] can process digital signals following the user specified algorithms to implement various gate operations, filters, mixer etc. In new series, FPGA has Random Access Memory (RAM) and First-In First-Out (FIFO) for data buffering, large digital signal processing (DSP) slices for high performance filtering, clock management tiles for direct digital synthesis (DDS) implementation and large number of input/output (I/O) pins for performing fast data interface. Due to versatility, FPGA has gained immense boost because it is easily configurable and reconfigurable without any hassles of soldering or desoldering. Digital signal processing is insensitive to noise pickup during execution of the algorithms, which enhances overall performance of the device. Nowadays, FPGA based devices are popular as their customization enable the experiment to reach beyond the level of standard analog devices, where high bandwidth is not a requisite. FPGA implementation of photon counting [85], real time data acquisition [86], arbitrary waveform generation [87], high quality filter [88] and so on are thus widely used. In particular to the lock-in amplifier and PID controller, which are focus of this article, Morris et al. first conceptualized the digital phase sensitive detection [89, 90]. In the area of lasers and optics, Sparkes et al. first showed scalable system for locking multiple optic parameters [91] and at the same time, laser frequency stabilization was demonstrated by Schwettmann et al. [92]. Since then, many groups have developed such systems [93, 94, 95, 96, 97] for improving the feedback quality [96].

Keeping these wide range of applications in mind, particularly for low bandwidth general applications [98], we have developed a compact, computer controlled instrument that integrates scan generator, lock-in and PID. A simple PID, in case of deviation beyond the servo's limit, becomes incapable to lock any more at the setpoint. In order to retrieve the lock without any human intervention, we have also developed an automatic relocking mechanism, which enhances effectiveness of an experiment with minimum interruption. Earlier, analog as well as digital control circuits have been demonstrated for automatic relocking laser frequency to transmission spectrum of a resonant Fabry-Perot cavity [99, 100]. Laser frequency stabilization is also demonstrated by using a complex pattern matching algorithm of atomic absorption spectrum programmed in a digital auto lock controller [101]. Such algorithm relies on proper fitting to the spectral feature as the instrument can be inefficient due to improper fit. In comparison to these, our relock mechanism is simpler, robust, faster and generalized, which as a whole decreases the dead time upon unlocking of the set parameter. In this chapter, we have therefore given description of hardware, software and user interface of the instrument along with detailed characterization of each programmed module. The indigenously designed and developed device not only serves for our purpose but is also universally applicable in other experiments as well where there is a requirement of PID, lock-in amplifier an function generator. To justify the universality of our device we have demonstrated its use in two other experiments, Auger spectroscopy and low coherence optical tomography.

4.2. Design

The layout and arrangement of modules and submodules programmed inside the FPGA are shown in Fig. 4.1(a). The analog signal coming from an experiment (input) is first needs to be digitized for further Digital Signal Processing (DSP) inside the FPGA. It is first passed through an analog to digital convertors (ADC), after processing, it then goes through digitalto-analog conversion followed by low pass filter for noise suppression to make it useful (output 1 and 2). The hardware part of the device therefore performs digitization of the input analog signals, analog output generation and suppression of the high frequency noise. Suitable logics and proper algorithms are programmed in the gate-array circuit to make it a unique and fast operational device. A Python based user-friendly Graphical User Interface (GUI) is enrooted for remote controlling of the instrument and plotting the acquired data almost in real time. The following subsections describe the hardware, firmware and GUI designs in detail.

4.2.1 Hardware

The hardware part of module consists of two printed circuit boards (PCB), an FPGA evaluation board (Digilent CMOD A7 35T) and an interface board for bridging the gap between analog



Fig. 4.2: (a) Populated PCB of analog interface board between analog signals and FPGA, consisting of DACs, ADCs and amplifiers. (b) Front and back panel of developed instrument containing the assembled interface PCB and FPGA evaluation board (front panel contains (1) Power, (2-4) Input, (5) Monitoring the PID output, (6) Output of scan generator, (7) Feedback output, (8) Output of lock-in amplifier, (9) Monitoring of the input, (10) Output of the AC coupled feedback, (11) Function generator, (12) FPGA On/Off switch, (13) Dual colour LED for indicator, (14) DC offset adjustment, (15) Power management switch, (16) USB for programming the FPGA and (17) USB for remote controlling the instrument). (c) Schematic drawing of front and back panel of the box along with dimensions for fabricating the complete servo controller.

and digital signals. The evaluation board has 48 digital I/O for signal processing; 2 channel Universal Asynchronous Receiver-Transmitter (UART), one for programming and other for communication between computer and FPGA, a 12 MHz clock and an optional slot for 100 MHz clock if required. For fast processing, we have inserted and used a 100 MHz clock (ASEM1-100.000MHZ-LC-T). The interface board which decides factors such as noise, bandwidth and gain of input and output signals used for laser locking, consists of differential amplifier, buffer amplifier, RC type filters, ADCs and DACs. The board is designed such that no ground loops are formed due to interface of digital and analog signals. To avoid added noise in the circuit, separated ground planes for digital and analog signals have been built. The input signal first passes through a low noise buffer amplifier (MAX4200) and successively through an opamp based differential amplifier (MAX4304). The later fulfills two purposes of changing the DC voltage offset of - 5V to +5V and providing gain of 0 to 1000 to the input signal. The subsequent signal then leaps over a 12 bit 1 MSPS ADC which has a detection limit of 0 to 3.3 V and 1 mV resolution. The differential amplifier puts the input signal into that limit, making it compatible for the ADC so that no information is lost by ADC. The ADCs used in the circuit is a commercially available two channel Digilent PMOD AD1 which uses IC AD7476A and a 20 MHz clock for sampling. The digitized and processed output from FPGA is passed through a 12-bit DAC (DAC121S101) at 1.8 MSPS and 30 MHz clock speed for fast servoing. Another 16-bit bipolar converter DAC714 with a resolution of 0.1 mV and speed 85 kSPS is also available in the circuitry for precise but slow operation, such as waveform generation for laser frequency scan via tuning its piezo. The components used to design this FPGA module are listed in Tab. 6.2. The populated Printed Circuit Board (PCB) of the analog interface where all these components have been soldered and the 2U box with the front and back panel, where this board has been assembled has been shown in Fig. 4.2 along with the relevant dimensions.

4.2.2 Firmware

The modules used for Pound Drever Hall technique locking are programmed on the FPGA using VHDL *i.e.*, very high speed integrated circuit + Hardware description language in Xilinx platform (Vivado 2017). Implementation of various modules such as, function generator, lock-in amplifier, PID controller, IIR based high and low pass filters onto the FPGA have been accomplished using VHDL programme. All the modules are programmed to be triggered at rising edge of the system clock which give a controlled manner for DSP. The details of the algorithms are discussed in this section.

S.No	Items	Quantity
1.	CMOD A7 35T	1
2.	19 inch 2U Rack mount Box	1
3.	\pm 15 V and \pm 5 V SMPS	1
4.	6 Pin Female connecting wire	6
5.	USB-UART cable 1 m	1
6.	USB Female-Male adapter	1
7.	USB cable 5m	1
8.	Three color low power panel mount LED light	1
9.	10 K panel mount potentiometer	1
10.	Toggle Switch	1
11.	LT1175 SMD	2
12.	LT1763 SMD	2
13.	Buf-634 SMD	3
14.	MAX-4204	11
15.	MAX-4304	2
16.	DAC714	3
17.	PMOD AD1	1
18.	PMOD DA2	2
19.	6 pin header	6
20.	47 nH Inductor	4
21.	100 nF ceramic capacitor	50
22.	4.7 μ F polar capacitor	4
23.	$1 \ \mu F$ polar capacitor	4
24.	$10 \ \mu F$ polar capacitor	4
25.	10 nF ceramic capacitor	10
26.	$1 \ \mu F$ polar capacitor	4
27.	100k POT PCB mount	5
28.	27k SMD resistance	6
29.	100 SMD resistance	8
30.	1k SMD resistance	8
31.	150k SMD resistance	2
32.	47k SMD resistance	1
33.	10k SMD resistance	4
34.	12k SMD resistance	1
35.	3.2k SMD resistance	1
36.	442k SMD resistance	1
37.	Female BNC right angle PCB mount	9
38.	XLR Male PCB mount (Optional)	1
39.	Ethernet port PCB mount(Optional)	1

Tab. 4.1: List of items required and the parts number that we made for designing this servo controller.



Fig. 4.3: Simulation results showing a Lorentzian signal with (Magenta) and without(Blue) DC offset being used as (a) input signal to the lock-in amplifier module. (b) After mixing the signal with the bipolar reference signal (c) After passing through a low pass filter.

Function Generator

A high frequency sine/square wave is required in the locking technique for current modulation which is used for generating phase sensitive detection through lock-in amplifier. There is also a requirement of slow triangular/sawtooth wave for scanning/tuning the laser frequency and searching for resonance peak to be used as reference peak for stabilization. These required waveforms at different frequencies are generated via the module programmed in the FPGA using its internal clock and programmable counters. Different algorithms are used for generation of these waveforms. For square wave generation, we use a counter that repeatedly counts every tick of master clock upto the quotient of the master clock frequency to the square wave frequency. To generate the desired frequencies, we setup state logic that goes active HIGH and active LOW in every successive counting. This technique is also used for sinusoidal and triangular wave generation but there it counts the output for the triangular wave and a numerical value taken from the sine-table for the sinusoidal wave.
Parameters	Low Pass Filter	High Pass Filter	PID Controller
ΔT	$1/2^{21}$	$1/2^{19}$	$1/2^{22}$
Filter coefficient	$\alpha_L = f_c/2^{21}$	$\alpha_H = f_c/2^{19}$	
Input	(i) 24 bits(ii) 16 bits	16 bits	16 bits
Equation Output	61 bits	61 bits	61 bits
Main Output	(i)16 bits - 43 downto 28 (ii) 16 bits - 35 downto 20	16 bits - 35 downto 20	16 bits - 35 downto 20

Tab. 4.2: Values of coefficients and total bits allocated to parameters associated with the filters

Lock-in amplifier

The new generation lock-in amplifier generally requires a reference frequency which splits into two parts with a phase difference of 90° for complete quadrature study. When any input signal, the Fourier components of which is not known, is fed to the lock-in amplifier, multiplied with these two reference frequencies and passed through a low pass filter, two outputs, Xknown as in phase component and Y called as quadrature component are obtained. The spherical polar coordinates $R = \sqrt{X^2 + Y^2}$ and $\theta = tan^{-1}\frac{Y}{X}$ along with X and Y then gives complete information about the input. In our experiment, the input signal has only single fourier component ($cos\theta$ or $sin\theta$) and so single mixer and filters are required for phase sensitive detection, thus making it simpler. For digital implementation of these modules, a 12-bit signal is multiplied with a 12-bit reference produced by the function generator giving a 24-bit output, which passes through a low pass filter. The logic used for the IIR type filter is

$$O_n = I_n \times \alpha_L + O_{n-1}(1 - \alpha_L), \tag{4.2}$$

where, O_n and I_n are the n^{th} sample of the output and input data, respectively. Here, $\alpha_L = 2\pi f_c \Delta T (2\pi f_c \Delta T + 1)^{-1}$ with f_c being the cutoff frequency of the filter and ΔT is sample interval of the input, which is processing time of single input sample for the programming. For performance test of the lock-in amplifier, a frequency modulated Lorentzian signal of the form

$$F(t) = F_o + \frac{a}{b^2 + [c - (dt + e\sin(\omega_m t))]^2},$$
(4.3)

is propagated and processed through various sub-modules. Here, a, b, c, d are the numerical constants, $\sin(\omega_m t)$ is used to modulate the signal at frequency ω_m , F_o is the DC offset.

The effect of DC offset on output of lock-in amplifier is shown in Fig. 4.3, where two input signals with and without DC offset are considered in Fig. 4.3(a). On multiplication with a reference signal, the signal with DC offset gets amplified as compared to the one with null offset (Fig. 4.3(b)). Due to this extra amplification, additional noise ripples arise in its output signal, whereas the output of the signal with zero offset is smoother as shown in (Fig. 4.3(c)). The extra noise ripples are reduced by using a lower cutoff frequency low pass filter. In lock-in amplifier measurement, use of lower cutoff frequency low pass filter in output decreases the bandwidth of the measurement thus causing information loss. In our experiment, the signal input to the module has a small DC offset which creates noise in the output. If the DC offset is quite high, it is also responsible for data overflow during DSP. To avoid these complications, we use a bandpass filter using a combination of high and low pass filters before the digital mixer. The signal on passing through a high pass filter for smoothing the signal. The high pass filter is implemented using the equation,

$$O_n = \alpha_H O_{n-1} + \alpha_H (I_n - I_{n-1}), \tag{4.4}$$

where $\alpha_H = (2\pi f_c \Delta T + 1)^{-1}$. We use fourth order highpass filter where each of them follows Eq. 4.4. After this, the signal is multiplied to a reference $A_o \sin(\omega_m t + \phi)$, where ϕ is the additional phase relative to the reference. The modulated signal passes through a fourth order low pass filter, where the first stage is configured in 24-bit and three successive stages are in 16 bit, for efficient removing of the high frequency component. The rolloff of the filters do not have significant improvement with further higher orders. At the end of the lock-in amplifier's operation, the signal has the form,

$$F_1(t) = \frac{eA_o}{2} \frac{dF(t)}{dt} \cos(\phi).$$
(4.5)

This output of the equation is also called phase sensitive detection (PSD), which gives information about the derivative of the spectra. ϕ has to be optimized for better results. Algorithm to execute the low and high pass filters requires 1-8 and 1-13 rising edges, respectively, of the FPGA's clock pulse that operates at 100 MHz. This gives $\Delta T_L = 8/10^8$ s and $\Delta T_H = 13/10^8$ s for low and high pass filters, which are negligible as compared to $1/2\pi f_c$. Hence, α_L can be approximated to $2\pi f_c \Delta T_L \cong f_c/2^{21}$ and similarly $\alpha_H \cong 1 - 2\pi f_c \Delta T_H \cong 1 - f_c/2^{19}$. The bits allocation of parameters associated with the filters are given in Table 4.2.

PID Controller with Relock mechanism

The mathematical description to programme a PID controller is described in Ref. [102], where as per the user's requirement, there are three kinds of algorithms used for programming three

Type	α	β	γ
А	$(e_n - e_{n-1})$	$\Delta T e_n$	$\frac{(e_n - 2e_{n-1} + e_{n-2})}{\Delta T}$
В	$(e_n - e_{n-1})$	$\Delta T e_n$	$-\frac{(I_n-2I_{n-1}+I_{n-2})}{\Delta T}$
С	$-(I_n - I_{n-1})$	$\Delta T e_n$	$-\frac{(I_n-2I_{n-1}+I_{n-2})}{\Delta T}$

Tab. 4.3: Values of coefficient in different PID types

PID controller types *viz*. A, B and C type. These three types have been programmed in the FPGA using VHDL language [103]. The choice of PID is specific to their performances in an environmental disturbance as well as to the user's specific requirement. As for example, in type C PID, whenever there is a change in the setpoint, integrator (I) part of the controller gets activated first, followed by proportional (P) and derivative (D) modules. This avoids any overshoot during setpoint stabilization. Type C PID is therefore useful for our requirement. The general equation for PID controller is,

$$O_n = O_{n-1} + \alpha K_P + \beta K_I + \gamma K_D, \tag{4.6}$$

where values of α , β , γ for all PID types are given in Table 4.3 and the bit allocation for various parameters and input and output data intervals are given in Table 4.2. X_o is the setpoint and $e_n = (X_o - I_n)$ is the error signal, K_P, K_I, K_D are the proportional, integral and derivative gains coefficients, respectively. As in Eq.4.6, each submodule in PID require its gain coefficient and the choice of the gain coefficients is critical for obtaining a reliable and stable lock, which can be tuned from the PC, in our instrument. Dual stages of PID with DC & AC coupled operations and possibility of reversing their polarity have been implemented, which can be utilized as per the requirement. As an example, for laser frequency stabilization, dual stage PID is used for servo controlling of both piezo and current. In the AC coupled PID, we use an additional high pass filter at the output and a large K_I which prevents the system from sudden fluctuations and suppress the current controller noise.

We have also developed a simple additional algorithm in PID for those experiment where more than one setpoints are available in the experiment like laser frequency stabilization where the zero point in error signal is present for all spectra. A new relock mechanism feature is introduced in the PID, which records the uniqueness of the initial state, where PID is first locked. If during experiment, the lock point changes from one state to another with same setpoint value, then the corresponding algorithm helps the system to identify its initial state and stabilize/lock to it. In relock mechanism, plant output is used as reference when system is initially locked,

Digital Lock-in and PID controller							
		Wa	aveform Gener	ator ⊙on ⊖o	FF		
⊂ Sawtooth ©	° Square ° Sine	Frequency 1	Hz	Amplitude 0	mV	Offset 1500 mV	
		J	Lock-In Ampli	fier © on O off			
Filters			Modulation		0	utput	
Input HPF cutoff	1000 Hz	0	🕆 Sine 🥤 Squa	re	Gain	1	
Input LPF cutoff	100000 Hz	I	Frequency 90000	Hz	Phase	632 deg	
Output LPF cutoff	10000 Hz	I	Amplitude <mark>7</mark>	mV	Filter Ore	ler 010203@4	
PID Controller (DC Coupled)	• on • off	PI	D Controller (A	AC Couple	d) ⊙ on ⊖ off	
K _P 21	Gain 151		<i>K</i> _P 11	Gain	251	Polarity C Positive C Negative	
K ₁ 456	set Point 0		K ₁ 976	Set Point	0	Ū.	
<i>K</i> _D	Туре СА	свес	<i>K</i> ₂ 0	HPF Cutoff	11	Туре САСВСС	
Interface © Windows	⊂ Linux	Port 12		CONNECT	ST₄	ART STOP	
Developed by CSIR - 1	National Physical	Laboratory, India				20	018

Fig. 4.4: The graphical user interface (GUI) panel for remote operation of the designed instrument. The GUI display consists of four sections which gives the option to (a) choose type of waveform, (b) operate the lock-in amplifier by controlling various parameters for filters, modulation and output, (c) optimize the controlling parameters of a PID, and (d) choose operating system and COM port.

then it is compared with the present value. There is a possibility that some small electronic fluctuations like DC offset or AC pickup noise may interrupt during the monitoring event of the relock module (RM) is going on, in such cases, the relock mechanism checks and compares the previous values and changes the reference plant value with respect to time. If there is large change in plant output with respect to present value then it switches OFF the PID controller and ON the Scan generator and searches for the initial lock state by changing the offset of the scan. During each scan of RM, it locks the system and takes the plant output and compares it to the reference. If it does not match, then it changes the offset of scan and again locks to another point until the plant output matches with the reference value. In this way it searches the initial state and if it not able to find it due to voltage limitation then it switches OFF all the PID controller used for stabilizing the system.

4.2.3 Graphical User Interface

In our servo controller, there are many modules and submodules which require input parameters for waveform generator, lock-in, PID once at a time or simultaneously for stabilization of the system. The parameters input to the module is given through computer using a user friendly GUI. The designing of the GUI is done in Python 3.7 platform, and serial communication protocol is used between computer and FPGA with 9600 baud rate (depends on user



Fig. 4.5: Frequency response of the first (green), second (magenta), third (cyan) and fourth (blue) order of (a) low and (b) high pass filters. (c) Lock-in amplifier's output using the first (green) and fourth (blue) order filters and (d) its corresponding noise density.

requirement) as shown in Fig. 4.4. An FTDI chip is present in the FPGA board which converts USB signal to RS232 using USB-UART connectivity wire. The bit allocation for each input in GUI is 32 bit which is further divided into four equal parts and each part is 8 bit which is one of the necessary conditions for serial communication. As soon as the parameters are received by FPGA, they are restored as 32 bits and finally passed to the respective module. The important part for programming the GUI in serial communication is to follow the same pattern serially for transmitting data from computer to FPGA and receiving it back from FPGA to respective modules.

4.3. Characterization and Discussion

Various modules and submodules in the developed servo controller need to be characterized for better measurement and stabilization of the system. All modules are programmed to be triggered at rising edge of the master clock and the input data are sampled through a 1 MSPS, 12 bit ADC of resolution 1 mV which is able to detect signal upto 100 kHz with a noise bandwidth of 500 kHz as per the Nyquist theorem [104] giving a quantum noise 12 μ V. The system's bandwidth and noise during the DSP is completely dependent on these ADCs; for improving these parameters for use in other application, PMOD AD1s are to be replaced by other ADCs



Fig. 4.6: (a)Schematic of optical setup for Rb saturation absorption spectroscopy (b) Optical setup use in experiment to characterize the servo controller

having serial peripheral interface (SPI).

The waveform generator in the servo controller can generate square, sinusoidal and sawtooth waves of frequencies of 0-100 kHz, with 1 Hz resolution and at an amplitude 0-3.3 V with 0.8 mV resolution. The noise density is measured to be $15 \text{ nV}/\sqrt{\text{Hz}}$ @ 10 kHz and harmonic distortion is 10 dB lower than the original signal strength. There is also another slow and precise DAC present in servo system which generates frequencies of upto 10 kHz with peak-to-peak amplitude of 5 V and a resolution of 0.1 mV. The slow and fast functioning frequency generators are used for scanning laser piezo to search resonance spectra and current driver for measurement of PSD or error signal, respectively.

Characterization of IIR filter is an important part in our servo system, and the quality of



Fig. 4.7: (a) Characteristic response of the type-C PID (green) due to insertion of a square wave (blue) as an external disturbance. (b) PID closed loop gain (magenta) and its corresponding phase change due to insertion of noise (blue).

filter completely depends upon its transfer function which is used to program any filter. The programmed transfer function of IIR type first order filter is given in [105], which shows that the amplitude of any frequency passed through a filter gets changed by an amount $L = 10 \log(1+(\omega)^2)$ called loss. The difference in loss between the fundamental frequency and its second harmonic is given by $\Delta L = 20 \log (2\omega/\omega)$ (if $\omega > 1$). We use this calculation as a characterization parameter, for example if we use a sinusoidal wave of frequency ω and pass it through a first order low pass filter of cutoff frequency $\omega < \omega_{cutoff} < 2\omega$, then as per the given theoretical formula, $\Delta L = 6$ dB. According to our calculation, if we implement the correct higher order transfer function, the losses will be multiples of 6 dB/octave *i.e.* 12 dB/octave and 18 dB/octave for second and third order filter, respectively. These losses give information about the steepness of the bode plot, also called as roll off of the filter. In our module, we

S.No	Parameters	Waveform Generator	Lock-in Amplifier	PID
1.	Damage threshold	\pm 6 V	\pm 6 V	\pm 6 V
2.	Resolution (I/O)	12 bit	12 bit	12 bit
3.	Output	0-3.3 V	$0-3.3 \mathrm{~V}$	0-3.3 V
4.	Roll off		6 dB/Octave	
			and 24 dB/Octave	
5.	Phase shift		0 to $360 \deg$	
6.	Phase resolution		$0.02 \deg$	
7.	Noise @ 10 kHz		$49\mathrm{nV}/\sqrt{\mathrm{Hz}}$	
8.	Modulation frequency	DC to 100 kHz	DC to 100 kHz	
9.	Modulation type	Square, Sine,	Square and Sine	
		Triangle, Sawtooth		
10.	Bandwidth			100 kHz with
				relock mechanism
11.	Signal latency			$5.7(1) \ \mu s$
12.	PI corner frequency			$10 \mathrm{~kHz}$
13.	$K_P, K_I \text{ and } K_D \text{ gain}$			upto 60 dB

Tab. 4.4: Specification of our FPGA based frequency generator, lock-in amplifier and PID controller

programmed upto fourth order filter, the Bode plots for first, second, third and fourth order low and high pass filters are shown in Fig. 4.5 (a,b), respectively. The fourth order filter enhances the roll off to 20.16(1) dB/Octave (theoretically 24 dB/Octave) as compared to 5.2(1)dB/Octave (theoretically 6 dB/Octave) in first order filter. The difference between theoretical and experimental value arises as all the coefficients for implementing the filter transfer function are approximated in digital logic as also mentioned in Tab. 4.2.

The detailed performance of the lock-in amplifier is tested in a Rubidium (Rb) Saturation Absorption Spectroscopy (SAS) [106]. In the SAS experiment, we use a 780 nm laser (output power 30 mW) which excites D_2 transition of the Rb atom. The schematic for SAS and the optical setup used for performing the experiment are shown in Fig. 4.6. On passing through a polarizing beam splitter (PBS) and half-waveplate, we get two parts of 29.5 mW and 0.3 mW. For SAS out of the 0.3 mW light, a probe beam of 25 μ W and a pump beam of 270 μ W are being used, with probe beam being monitored on the oscilloscope via a photodiode amplifier. A 1 Hz triangular wave is fed to the piezo driver as per which the laser frequency gets scanned. The SAS spectra of Rb atom can be detected on the oscilloscope as shown in Fig. 4.8(a). During tuning of laser frequency using piezo driver, we connect a 95 kHz sine wave of amplitude 25 mV to laser current driver for high frequency modulation. When laser is scanned and photodiode gives a voltage variation in form of a spectrum, then using this high frequency modulation and lock-in amplifier, we get the phase sensitive detection of the corresponding spectra. When SAS signal is detected through photodiode and digitized using ADC, it first

S No	Parameters	Our Module	Stanford Research	Zurich Instrument	Toptica
D. NO.	1 arameters	our module			Topuca
			Lock-in SR830	HF2LI	Digilock
1	Input voltage	± 5	1 V	\pm 3.3	± 3.5
	limit				
2	Bandwidth	100 kHz	102 kHz	50 MHz	21 MHz
-	Danaman	100 1111	102 1111	00 101111	
2	Input voltage	$1 \mu V$	2 nV	1 nV	
3	input voltage	1μ v	2 11 V	1 11 V	
	sensitivity				
		0	<i>c</i>		
4	Input gain	10^{3}	106	auto gain	digital gain
5	Filter	24 dB/octave	24 dB/octave	48 dB/octave	12 dB/octave
	Roll off				
6	Function generator	Sine Square	Sine	Sine	Sine triangle
0	1 unetion generator	Triangle contecth	bille	onic	square sourceth
		mangle, sawtooth			square, sawtooth
-			DT Á		
1	PID controller	PID bandwidth	NA	PID bandwidth	PID bandwidth
		100 kHz and		5 kHz	1 MHz and
		auto relocking			auto relocking
8	Interface	RS232	RS232	RS232	RS232
9	Updates	Easily updates	No	Easily updates	Easily updates
	opulitos	Easily apartees	110	Eachly aparates	Easily apaatoo
10	Noise	$30mV/\sqrt{Hz}$	$6mV/\sqrt{H_{\pi}}$	$5mV/\sqrt{Hz}$	$140 m V / \sqrt{H_{z}}$
10	INDISE	$30\pi V/\sqrt{\Pi Z}$	$0\pi V/\sqrt{\Pi Z}$	$\frac{\partial nv}{\sqrt{\Pi z}}$	$140mV/\sqrt{Hz}$
		@10kHz	@IKHZ	@10kHz	@1kHz
11	Application	Universal	Universal	Universal	Only for Toptica
					laser controller

Tab. 4.5:	Comparison	of our	module to	different	commercially	[,] available	devices
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passes through a band pass filter in lock-in amplifier for smoothening the signal by removing all low frequency terms. This signal is passed through a mixer where it gets multiplied to a reference signal of frequency same as that of the high frequency modulation and then passed through low pass filter of cutoff at 1 kHz. The lock-in amplifier's performance and their noise density while using first and fourth order filters are shown in Fig. 4.5 (c,d). It is clear that the fourth order filter enhances S/N and the noise density is reduced to $30 \text{ nV}/\sqrt{\text{Hz}}$ @10 kHz. The additional peaks at higher frequencies, as visible in the noise spectra shown in Figure 4.5 (d) are characteristic of the DAC, pickup noises by the internal circuit and due to the clock jitter.

In our module, we programmed three different PID controller types, A, B and C, for generating feedback signal for setpoint stabilization. Among these, we are using the C type owing to its ability to avoid overshoot during laser stabilization. For characterizing the programmed PID, a square wave is used as an input disturbance signal and the PID is set such that the output is a DC voltage *i.e.*, the PID produces another square wave of different amplitude and 180° out of phase. It is observed in Fig. 4.7(a) that the PID flicks out of the lock with a noise pulse appearance but relocks in due course of time based on the amplitude and frequency of the introduced disturbance. As for example, PID relocks within $135(2)\mu$ s for a noise pulse of 131 mV at 1 kHz. The linear response of PID briefs that it fails to relock if the noise is > 1 mV at 1 MHz, which is actually the resolution and speed of the ADC. Therefore, the PID speed is limited by that of the ADC's. The Bode plot of the PID controller in Fig. 4.7(b) shows that the closed loop gain decreases due to ADC, and the phase changes suddenly near 100 kHZ frequency. The detailed specification of the servo controller and its modules are given in Tab. 4.4. Further, in Tab. 4.5 we show some of the essential parameters of our developed system and compare them with few commercially available similar systems. In brief, even though performance wise our system is similar to commercially available systems, it lacks in input voltage sensitivity, which is mostly not so much important other than few specific applications.

4.4. Laser frequency stabilization

Even though the instrument is universal, in our experiment that is developing single ion based optical frequency standard, we plan to use this to stabilize laser frequency with respect to any reference point. The reference point in our experiment is a Doppler free spectrum obtained SAS of Rb atom using 780 nm New Focus vortex TLB6800 ECDL laser which probes its D_2 line. Optical setup for saturation absorption spectroscopy for Rb is shown in Fig. 4.6(a) and the arrangement of optical items in optical breadboard are also shown in Fig. 4.6(b). The laser has a piezo and current driver bandwidth of 1 kHz and 1 MHz, respectively which is used for providing feedback signal to stabilize the laser frequency. In the SAS spectroscopy, the 26 μ W probe beam passes through Rb vapour cell in presence of the counterpropagating pump beam of 290 μ W and is detected on a fast photodiode. For laser frequency stabilization, a 1 Hz, 500 mV triangular wave and a 96 kHz, 20 mV sinusoidal wave is fed to the laser piezo driver and current driver, respectively from the servo controller. The signal connected to piezo is used to tune the laser frequency and the DC offset to search for the resonance peak of Rb atom. The absorption spectra and the SAS signal with error signal corresponding to each spectra is shown. A suitable peak is chosen as reference point for locking and is scanned, eventually the PID is set ON and the laser frequency is locked at top of the peak of our reference spectra. The error signal which is the output of the lock-in amplifier is first characterised. If the modulation frequency is changed after error signal generation, we observe an increment/decrement occurring in its amplitude due to phase change between reference signal and the modulation signal. Some of the previous works state that low frequency modulation has been used for laser frequency stabilization because it gives maximum slope of error signal *i.e.* larger slope near the zero crossing which results in a tight locking but Eq. 4.5 theoretically states that the slope of error signal and modulation frequency is completely independent of each other. Experimentally, we found that for a certain change of modulation frequency, there is a change in slope



Fig. 4.8: (a) Doppler broadened Rb-Saturation absorption spectrum consisting hyperfine transitions of the D2-line (red) and their lock-in detection (blue). (b) Laser wavelength during lockunlock-autolock demonstration (red). (c) Demonstration of self finding automatic relocking to a desired setpoint where photodiode voltage (green), laser piezo voltage (magenta) and corresponding error signal (blue) are shown.

of the error signal near zero crossing as calculated and plotted in Fig. 4.9 but just by using a phase shifter, the slope can be corrected and maximized by phase optimization. Therefore, Fig. 4.9 reveals the independency of error signal slope to modulation frequency provided the phase is corrected.



Fig. 4.9: Slope of error signal changes with respect to change in modulation frequency (blue) but is corrected (cyan)using phase shifter. A gaussian function is also fitted to show the variation of slope of error signal with respect to frequency (orange) due to phase change between reference and modulated signal. Theoretically the change of error signal slope is independent of modulation frequency which is fitted in the plot (green).

The modulation frequency gives a way to increase the bandwidth of the PID controller. Increase in the modulation frequency increases the cutoff frequency of low pass filters thus increasing the bandwidth of PID controller. The bandwidth of the PID controller in laser frequency stabilization is nearly equal to cutoff frequency of low pass filter used in lock-in amplifier's output to generate error signal. As we increase the bandwidth of the PID we increase the locking time for the module but decrease the tightness of the lock point. The error signal which is used for locking becomes more noisy which is clearly observable in noise spectra of error signal. When an error signal is generated at a given modulation frequency, then we ON the PID controller and observe the noise fluctuation of error signal and tune the gain coefficient of the submodule in PID controller to minimize these fluctuations and make system stable. For further minimizing the error signal and photodiode voltage fluctuation we use a second AC coupled fast PID controller which gives feedback to current driver to prevent fast pickup noise to the laser and avoid unlocking from its initial state. When laser is locked and error signal fluctuation is minimized, we take the data of power spectral density for error signal and photodiode signal at a cutoff frequency of 10 kHZ for generating error signal as shown in Fig. 4.10. By analysing the error signal using rectangular noise density approximation, we find that the linewidth of the laser is 0.5(1) MHz.



Fig. 4.10: Noise Spectral Densities of input signal (grey) and Lock-in amplifier's output (cyan), DAC output (blue) and spectrum analyser's noise floor (magenta).

Once the laser is tightly locked, the next measurement is for the durability of the lock state. The durability of lock state is completely dependent upon the environmental conditions. If laser frequency gets drifted such that the voltage required for stabilizing it back to the setpoint is beyond its limit, then it gets unlocked or may be locked to another peak. At such instances, a smart relock module is required which relocks the laser back to its initial reference point. In this servo system we developed a simple and fast relocking module, which is activated when laser is locked. In locked condition, the photodiode signal shows a stable DC voltage which is continuously monitored by the relock module, by using the difference between present and past value. If the difference is large, the PID turns OFF and scan gets ON to research for the peak. In every scan process it turns the PID ON to compare the value of photodiode signal to the previous one when laser was initially locked. If this value matches, scanning is stopped. The process of lock and relock is shown in Fig. 4.8(b) and (c) where we show the data of wavelengthmeter and oscilloscope during the relock process. In this particular example, to start with, the laser was locked to peak-1 and in due course of time it jumped to lock at peak-4. In this case,

it was externally forced to set the laser unlocked to demonstrate automatic relocking, since otherwise experimentalists will be occupied round the clock and wait for laser to get unlocked. In the successive steps, as described earlier, the instrument itself starts to search for the defined lock point which goes on until the difference between the changed photodiode signal and the reference is null and the laser comes back to the initial lock point.



Fig. 4.11: Measured signal showing performance of the instrument in Auger spectroscopy where input (red), output via lock-in detection (blue)

4.5. Auger electron spectroscopy

To prove experimentally that our lock-in amplifier module can detect signals buried deep under noise, we tested this instrument's performance in Auger spectroscopy, where the required signals are in range of few tens of μ V to nV. In this experiment, weak signal of Auger electrons emerging from a silicon substrate Si (III)- 7 × 7 is detected using the lock-in amplifier. The electron beam of 2 keV and the current at 2 μ A with 1 V peak-to-peak multiplier voltage is used. We used 17 kHz square wave with 10 V peak-to-peak to modulate current of the electron gun. The detected voltage corresponding to the Auger current is used as the input to our instrument after the DC offset cancellation and 100 times amplification. The input to the lock-in amplifier and the processed output are shown in Fig. 4.11. The input consists of modulation frequency as well as noise inartistic to the signal coming from the detection system and electron gun [107]. As visible in the Figure 4.11, the desired signal appears with very good S/N through the lock-in detection, which is otherwise not resolvable.



Fig. 4.12: Measured signal showing performance of the instrument in Low coherence optical tomography.

4.6. Low coherence optical tomography

The instrument is used in a low coherence optical tomography setup for surface and sub-surface imaging of materials and biological samples. The detailed protocol of the experiment can be found in Ref. [108]. In this experiment, photons scattered due to impinging of light on the sample surface (solid curved metal used in nuclear reactor) are detected by our servo controller. The speed of scanning the surface of the sample creates a modulation in the output signal which is amplified using an analog amplifier which provides variable gain from 1-1000. Due to this amplification, ADC used in our servo controller is able to detect weak signals upto > 50 μ V. This signal is multiplied with the reference signal whose frequency is theoretically calculated using speed of the scanning system. The demodulated signal obtained by the lock-in amplifier of our module, as shown in Fig. 4.12 is used for imaging the surface of the sample. The process of reconstruction of the surface image is described in Ref. [108].

4.7. Conclusion

In this chapter, we describe the design, performance, characterization and universality of the FPGA based servo controller. In this servo controller, we have programmed function generator, lock-in amplifier and PID controller with its unique feature of self finding of a defined setpoint and auto relocking. The digital implementation and software processing not only makes the instrument compact and cost effective but opens up possibility to customize by reconfiguring it for some specific application. A Python based graphical user interface is developed for

controlling of the instrument from a remote PC and monitoring it almost in real time. The waveform generator is capable to generate sawtooth, square, triangle and sinusoidal waves upto 100 kHz, which does not drift with environmental parameters and is free from harmonics due to digital implementation of the modules. This system incorporates low and high pass digital filters having 24 dB/octave roll-off that brings down the lock-in amplifier's noise density to $49 \,\mathrm{nV}/\sqrt{\mathrm{Hz}}$ @ 100 kHz. This instrument is designed in such a way that it gives freedom to connect any of the modules to the other without getting any conversion delay or noise introduction. Selection of the devices can be done from the GUI itself without reconfiguring the firmware. Performance of the instrument is demonstrated for laser frequency stabilization, where we use the function generator and lock-in amplifier for generating the error signal and the PID controller to lock the laser frequency. The instrument is capable of resolving > 1% Rb saturation absorption signal relative to the doppler broadened spectrum, which was used as the frequency reference. Further, to check universality of the instrument, we used it in Auger spectroscopy and low coherence optical tomography experiments, as well.

Chapter 5

Frequency Stabilization of Multiple Laser Wavelengths to a Common Atomic Reference via Transfer Cavity Locking

Publication: A. Roy, L. Sharma, S. Panja and S. De, Multiple laser frequency using a single actively stabilized Fabry-Perot cavity, manuscript is under preparation (2019).

5.1. Introduction

The atom-photon (laser) interaction is a versatile way to perform experiments, such as, precision spectroscopy, laser cooling and trapping, production of degenerate gases, quantum optics, quantum enhanced technology, preparing qubits and interconnecting them and so on. Reproducibility of the measurand that are obtained through any of these experiments rely strongly on the lasers frequency and its stability together with other associated parameters. Therefore, continuous efforts have been made not only to improve quality for the laser frequency stabilization but also to achieve longer-term stability. Depending on the stringent requirement, various methods starting from very complex to rather simple techniques have been tried. Few experiments need ultimate frequency stability [111], e.g., 10^{-19} per s [112], where as few 10 MHz -GHz per day stability serves for most of the cases. Thus, for the laser applications, a simple system that is easy to operate and maintain is more desired. Commonly used techniques are, Saturation Absorption Spectroscopy (SAS) of an atomic or molecular transition and referencing the laser frequency with respect to that [113]. Pound-Drever-Hall (PDH) is an another simple technique to frequency stabilize a laser with respect to a stable resonant Fabry-Perot (FP) cavity transmitted spectrum [114]. Using this approach, stability can be further enhanced to a state-of-the-art level by using a ultra-stable FP cavity [115], which consists of a more complex setup. Upon availability of an optical frequency comb, lasers of different wavelengths can be frequency locked with respect to the nearby comb lines, by measuring their beat frequency and servo-ing that to the experimenting laser [116]. In recent times, software based signal process-



Fig. 5.1: Block diagram of the transfer cavity locking, where λ_R is the reference laser and frequency stabilized by saturation absorption spectroscopy and λ_{Si} (for i = 1,2..) are the slave lasers that are to be frequency stabilized with respect to the stable Fabrt Perot cavity. The setup allows multiplexing so that several wavelength can be stabilized using single reference.

ing to stabilize the laser frequency by using a wavelength-meter is also available now-a-days, which is maintenance free and very user friendly [117]. All of these above mentioned techniques have their Pros & Cons, which are not discussed here as that is not focus of this article.

Many experiments involve multiple lasers at different wavelengths, such as laser cooling of atoms with complex energy level structure, multi photon optical spectroscopy and so on. There, frequency stabilization of the individual lasers are generally achieved by individual techniques, which elaborates the overall setup and extend complexity of the workhorse. As for example, laser cooling of barium [118], radium [119], ytterbium [120], strontium [121], strontium-ion [122], ytterbium-ion [20] and so on involve multiple laser wavelengths. As an example, for ytterbium-ion, transitions at 369.5 nm, 935.2 nm and 638.6 nm/760 nm wavelengths form a closed cycle for laser cooling where as forbidden transitions at 435.5 nm and 467 nm wavelength are used to prepare optical qubit or as the clock transitions. Laser systems at these wavelengths are commercially available. Out of these lasers, Extended Cavity Diode Lasers (ECDLs) at 369.5 nm, 435 nm and 467 nm are typically frequency-doubled systems hence their fundamental wavelengths at 739 nm, 870 nm and 934 nm, respectively, lies within possible anti-reflection coatings of an FP mirrors together with the others. For such applications, frequency stabilization of all lasers to a common reference will simplify nitty-gritty of the system by manifold. Keeping that in mind, in this article, we show a proof of concept of possible multiple laser frequency locking with respect to D2 transition of radium atom using the transfer cavity locking technique [123]. In this setup, we further demonstrate co-scanning of the laser(s) frequency just by tuning the reference lasers frequency and fine tuning of the individual lasers frequency

by tuning their reference point. These simple techniques allow to shift detuning of the lasers in an experimental sequence that typically follows photoionization, laser cooing, state initialization by optical pumping, state detection and so on. In this chapter we give experimental demonstration of the described technique.

5.2. Theory

In transfer cavity technique, frequency stabilization of a slave laser involves three steps: locking the reference laser with respect to a relevant atomic transition followed by coupling that frequency stabilized laser to a FP cavity and stabilizing the optical distance between the cavity mirrors so that reference laser is perfectly mode matched to it and finally locking the slave laser's frequency that can be mode matched to the locked cavity length. Thus, in this technique, characteristic stabilization of the reference laser is transferred to a slave laser using an intermediate FP cavity, schematic of the transfer cavity locking is shown in Fig. 5.1 . Stability of the slave laser's frequency and noise incorporates characteristics of the reference laser and the FP cavity.

In this locking technique, frequency stabilization of the slave laser depends completely on the stability of the modes formed in the FP cavity. The modes are formed inside the cavity due to the constructive interference of light oscillating inside the cavity. The condition for constructive interference inside the cavity is

$$n\,d\,\cos\theta = m\frac{\lambda}{2},\tag{5.1}$$

where *n* is refractive index of the medium, *d* is separation between the mirrors inside the cavity, θ is angle between incoming light direction to the normal of the mirror, *m* is integer and λ is wavelength of the light. It is clear from this equation that mode stability depends upon dynamicity of optical distance between cavity mirrors and stability of the alignment of light to the cavity. For both short and long term stability, refractive index within the cavity, geometric separation between the mirrors and overall alignment ought to be stabilized. Depending on their geometry and the type of mirrors used, optical cavities are of five types: plane-parallel cavity formed by using plane mirrors; concentric, confocal, hemispherical and concave-convex type optical cavities made by using different arrangements of concave, convex and plane mirrors. The stability of these cavities are determined using the equation

$$0 < g_1 * g_2 < 1, \tag{5.2}$$

where, $g_i = 1 - L/R_i$, L is cavity length and R_i is the radius of curvature of the i^{th} mirror used in the cavity [124]. Equation 5.2 shows that the convex-concave type cavity is comparatively more stable and for easier alignment of laser light and cavity, convex-convex type confocal optical cavities are preferable but use of parabolic mirrors for better alignment purposes is the best choice due to their convergence properties. It is very sensitive to small changes in distance between the mirrors. The frequency modes of the cavity made of parabolic mirror is

$$f_{q,m,n} = \frac{c}{2L} \left(q + \frac{m+0.5}{\pi} \arccos \sqrt{g_{1x}g_{2x}} + \frac{n+0.5}{\pi} \arccos \sqrt{g_{1y}g_{2y}} \right), \tag{5.3}$$

where, c is speed of light, L is length of the cavity, $g_{ij} = 1$ - L/R_{ij} and R_{ij} is radius of curvature of i^{th} mirror in j^{th} direction and q, m, n are integers. The cavity length is a major factor which decides system's stability. The material chosen for building the cavity should therefore have nearly zero ultra low expansion (ULE) coefficient at a particular temperature (T_c) and small variation of it with respect to temperature change in the vicinity of that T_c . Using a serve controller to give a feedback voltage to the piezo crystal of the cavity can also help in stabilizing the cavity length. Refractive index plays its role specifically if the medium is air because temperature, pressure and humidity fluctuations may also lead to frequency instabilities. If the modes formed in the cavity are in the same position, then the fluctuation of the slave laser frequency due to change in environmental conditions is

$$\frac{\delta f_s}{\delta \alpha} = \left[\frac{\left(\frac{\delta n_r}{\delta \alpha}\right) n_s - \left(\frac{\delta n_s}{\delta \alpha}\right) n_r}{(n_s)^2} \right] \frac{n_s}{n_r} f_s, \tag{5.4}$$

where, α represents environment parameters, f_s is frequency of slave laser, n_r and n_s are refractive indices of medium with respect to reference and slave laser, respectively. To avoid the frequency instabilities due to refractive index, the cavity should be placed in vacuum or the reference laser wavelength should be chosen as close as possible to that of the slave laser to avoid frequency instabilities due to change in refractive index. In our experiment, we use 780 nm as the reference laser and plan to stabilize 739, 760 and 935 nm as slave lasers for laser cooling of Yb ion following the scheme as shown in Fig. 5.1. Here as a proof of concept we describe frequency stabilization of the 739 nm laser via transfer cavity lock technique. After stabilization of slave laser frequency, an important parameter to be measured is the linewidth of the locked laser which gives the tightness of our locking system. Details of different techniques for measurement of laser linewidth via self heterodyne method, rectangular noise spectrum and arbitrary noise spectrum are given in Ref. [125, 126]. Using these, we can measure the laser linewidth as well as carry out fine-tuning of the PID and lock-in amplifier parameters to frequency uncertainty.

5.3. Experimental requirements

In this experiment, the reference laser at wavelength 780 nm is an ECDL laser (Vortex Plus 6800 TLB -6813, New Focus) with a wavelength tunability from 779 nm to 781 nm. To stabilize



Fig. 5.2: (a) 780 nm reference laser, (b) Slave laser 760 nm/ 935 nm, (c) 739 nm slave laser which generates 369 nm using SHG, (d) Fast photodiode with ethernet connectivity, (e) Toptica FPI 100 FP cavity, (f) Servo controller for cavity, (g) Rb vapour cell, (h) Servo controller for reference laser. its frequency we perform SAS in a Rb vapour cell and the FP cavity is stabilized with respect to this reference laser. At present, to demonstrate the technique, we use the slave laser (Toptica SHG pro laser) of fundamental wavelength at 739 nm. The transfer cavity used for stabilizing 739 nm is a FP cavity (Model No. FPI 100, Toptica). The cavity has a cutoff frequency of 79 kHz and a Free Spectral Range (FSR) is 1 GHz. For signal detection of modes and SAS spectra, we use our indigenously developed fast photodiodes circuits whereas an inhouse developed FPGA based lock-in amplifier and PID controller is used for servo controlling (described in Chapter 4). The instruments and devices used in the experiment are shown in Fig 5.3. Along with that, various standard commercially available optics and opto-mechanical components like PBS, mirrors, polarizers, *etc.* have also been used for setting up the experiment.

5.4. Experimental setup

Figure 5.3 shows the optical setup of the transfer cavity locking. Its first part depicts stabilization of reference laser frequency with respect to the D2 line of Rb by employing SAS. The output laser at wavelength 780.02 nm has 30 mW power in total when laser current is 84 mA and temperature is 27 ^{o}C . This output beam is passed through approximately a 90:10 beam splitter (BS). The reflected light of 2.7 mW is used in the SAS set-up while the transmitted beam of 27 mW is further splitted into two parts using a combination of half waveplate and a polarising beam splitter (PBS), of which the reflected part of 8 mW is double passed through an Acousto Optical Modulator (AOM) operated at 200 MHz and 1 W RF power. Output from the AOM is then coupled to a single mode polarization maintaining optical fiber and used it as input to the FP cavity. The purpose of AOM here is to generate high frequency modulation use for stabilization of FP cavity and tunability in the 780 nm laser frequency. The setups of SAS and Fabry-Perot cavity are assembled on two separate 600 mm \times 300 mm size optical bread boards for transportability and then breadboards are firmly mounted on the optics table. The intermediate single mode polarization maintaining fiber ensures redundant optical alignment between them. In SAS spectroscopy setup, using a half waveplate, the power of the 2.7 mW reflected light is adjusted to 290 μ W and is used as the pump beam. The p-polarized pump beam after single pass through the Rb-vapour cell further passes through a Neutral Density (ND) filter followed by a quarter waveplate and a plane mirror. Then the s-polarized light with reduced power of 26 μ W is reflected back to the Rb cell as the probe beam and counterpropagating to the pump beam which is then detected by one of our indigenously developed fast photodetectors made of silicon pin photodiodes. For a closed feedback loop for laser frequency stabilization, the photodiode signal is connected to the FPGA based lock-in and servo controller, the output of which is then sent to the laser controller. The indigenous FPGA based servo controller is described in Chapter 4. The fiber coupled light of 780 nm and power 2.7 mW is sent into the FP cavity through a half waveplate and a PBS.





The reflected light coupled to the scanning FP cavity falls on a commercial fast photodiode, the output signal of which is sent to the piezo driver circuit of the FP cavity via the servo controller, thus completing the feedback loop for stabilization of optical distance between the cavity mirrors using pre-stabilized 780 nm laser via SAS. The next part is to couple the slave laser (in this case 739 nm) to the same FP cavity for which its 30 mW laser output is adjusted to 10 mW using a combination of halfwave plate and a PBS. The 10 mW laser beam is passed through an AOM that operates in double pass configuration, which is operated at 200 MHz with 1 W RF power. The first order diffracted light from this AOM is double passed through the quarter waveplate, reflected back to the AOM and then sent through a halfwave plate and PBS to couple it into the FP cavity. In this commercial laser system, current modulation is used for stabilizing the SHG (second harmonic generation) cavity that generates the 369.5 nm light. Due to this reason, we opted to use an additional AOM for fast frequency modulation in laser light without disturbing the main laser system to generate error signal which is used to stabilize the 739 nm laser frequency. Use of an additional AOM also increases the range of tunability. Transmitted light from the FP cavity decouples from the 780 nm reference laser by a PBS and finally falls on a second photodiode. The corresponding electronic signal becomes a part of the feedback loop to stabilize the frequency of the slave laser. Figure 5.3 shows that three servo controllers are required for operating the entire transfer cavity locking technique. An indigenously developed FPGA based device serves for this purpose where all the three servo controllers are modeled onto a single FPGA chip, whose characterization and working principle of each module has been described in Chapter 4.

Figure. 5.4 illustrate the block diagram of the entire FPGA based servo controller that is used for transfer cavity locking experiment. Out of three, each of these servo controllers consist of lock-in amplifier, function generator and PID system. Details of the modules construction and the signal processing within the controller has already been discussed in Chapter 4. In this module we use 1 MSPS speed analog to digital convertor (ADC) and digital to analog convertor (DAC) AD7476A and DAC121S101, respectively. Additionally we use a high voltage low noise amplifier using OPA189, which is able to generate ± 15 V with 1 MHz speed for stabilizing FP cavity, because drifting speed of FP cavity is large and have 10 V/FSR. We have used the Pound Drever Hall type of locking technique which requires a high frequency modulation for generation of the error signal out of the cavity transmission spectra. For generation of error signal out of the SAS spectra, via lock-in detection technique, the laser's current controller is fed with a 89 kHz, 20 mV peak to peak sinusoidal wave, which is also used as reference for the lock-in amplifier. Our FPGA based instrument is capable of generating this frequency internally, hence additional frequency generator is not needed. This makes the entire system cost effective and low noise. Similarly, for stabilization of the cavity, an external frequency modulation is performed by feeding the AOM driver with a 96 kHz, 30 mV peak to peak sinusoidal





Fig. 5.5: (a) Optical setup of transfer cavity locking and for tuning the length of cavity is done by AOM by changing its RF frequency using change in voltage pin of the VCO. (b) The effect for changing the RF frequency of AOM, there is a shift arise in reference laser (red) transmitted mode which cause shift in slave laser mode (blue) and change in frequency of slave laser in control manner. In this process, the complete error signal shifted but the lock point is remain constant. (c) Optical setup of transfer cavity and for fine tuning the length of the cavity is done by side of the fringe locking technique. In this technique we change the setpoint of servo controller which makes a shift in locked frequency in control manner. (d) The effect during the transmitted modes of reference (red) and slave (blue) laser are shown. In this process reference laser is locked at tip of the peak of transmitted mode and we change the setpoint of transmitted mode of slave laser, which causes change in error signal offset and result a small change in frequency of slave laser without change in reference laser.

wave, which modulates the first order diffracted light. Apart from frequency modulation, use of AOM in this case also gives the advantage of drifting the length of the stabilized cavity by changing its operating RF frequency, thus providing freedom of tunability in our system. In Fig. 5.4, we only show the stabilization of 739 nm laser using transfer cavity locking technique, to demonstrate the proof of concept of the multiple lasers' frequency stabilization with respect to a common atomic reference.

5.5. Results and Discussion

In transfer cavity locking system, frequency stability of the slave laser(s) depends on frequency stabilizations of reference laser, optical length of intermediate FP cavity and the intrinsic stability of the slave laser. The noise associated to the slave laser's output frequency is not only limited by its intrinsic system noise but also by that of the frequency stabilized reference laser and intermediate FP cavity, which propagates all the way and adds up. Digital implementation



Fig. 5.6: (a) Wavelength meter data shows the steps for laser frequency stabilization of 780 nm laser using Rb SAS. The inset shows Rb SAS profile (orange) and corresponding lock-in detection (magenta) and blue line shows the situation when only SCAN controller is ON in servo controller, red line shows when SCAN and PID both are in ON state and green line shows when only PID is ON and SCAN is OFF. The inset figure shows the fluctuation of data in wavelength meter which estimates frequency stability of the locked laser. (b) Transmission mode profile of the optical cavity when 780 nm laser (red) and 739 nm laser (green) are coupled inside it and a triangular signal is connected to piezo driver (blue) which changes the length of the cavity.

Wavelength	FSR (GHz)	Finesse	$\triangle \nu_{fringe}$ MHz
805 nm (Spec sheet)	1	400	2
780 nm (λ_M)	0.985(8)	205(23)	4.8(5)
739 nm (λ_S)	1.007(8)	183(18)	5.5(5)

Tab. 5.1: Comparison of parameters measured experimentally for optical cavity used for transfer cavity locking with those provided in the specification sheet.

of the required electronic hardware particularly incorporating all the modules entirely in a single FPGA is therefore done to reduce picking up of the environmental noises. In transfer cavity locking system, there is requirement of tunability in laser frequency for employing frequency modulation. This can be achieved by using optical modulators like AOM and EOM which requires RF source, amplifier and various optics which increase the complexity of the experiment. In our transfer cavity locking technique, we demonstrate use of side of the fringe locking for tuning the laser frequency during stabilization process. In this technique, we are able to tune the laser frequency in a similar fashion as can be done by AOM. Figure 5.5 (a) and (c) shows the optical setups used for tuning the frequency of slave laser in transfer cavity locking when using AOM and side of the fringe locking, respectively and the difference in principle between tunability using AOM shifted lockpoint of the λ_R and side of the fringe is shown in Fig. 5.5 (b) and (d). These figures show the effect on transmitted mode and error signal during the locking process in both cases. In AOM control, change in RF frequency applied to the AO crystal shifts only the absolute wavelength of reference laser; the setpoint of error signal in the transmitted spectrum of FP cavity remains constant, hence the reference lock point gets shifted. Both the reference and slave laser frequencies change in this process keeping their relative frequency difference constant. In side of the fringe locking, frequency tuning of the slave laser is done by varying the setpoints along its transmitted mode locked with respect to the stabilized FP cavity.

The initial process of the transfer cavity locking technique is to stabilize the reference laser (780 nm) with respect to any of the atomic transition of the Rb atom. In Rb Saturation absorption spectroscopy, scanning the laser frequency gives two Doppler broadened absorption dips of ⁸⁵Rb and ⁸⁷Rb resulting from its D2 line, among which ⁸⁷Rb spectra is chosen as its corresponding SAS spectra are clearly resolved with greater separation between each hyperfine spectra compared to that of ⁸⁵Rb SAS spectra. In ⁸⁷Rb absorption dip, we get six such dips, three for transition and other three for crossover transitions. Among these, we choose the spec-



Fig. 5.7: Error signal generation from SAS (red), transmission mode of the cavity for reference laser (orange) and slave laser (green) for (a) top of the fringe and (b) side of the fringe methods.

tra corresponding to the transition $F = 2 \rightarrow F' = 3$ hyperfine transition for reference locking. In our setup, we do not use any balanced photodiode or modulation transfer spectroscopy, so the slope or shape of the error signal depends on the position of the Doppler free spectra lying inside the absorption dip. The chosen spectrum lies within the nearly flat region of the absorption dip, so there is negligible effect coming from the slope of the absorption dip. The natural line-width of the chosen SAS spectra is 5 MHz but due to power broadening of the pump beam light power and pressure broadening resulting from environmental fluctuations, obtained FWHM of this spectra is 20(1) MHz. Decreasing the power of pump beam in this cannot be a solution as that would further lower the signal to noise ratio. To stabilize laser frequency with respect to this spectra, we first reduce the scan amplitude of the laser, so that it scans only the single peak of chosen spectra. In this locking process, we want to stabilize laser frequency at top of the fringe which requires generation of an error signal of the chosen spectrum. The error signal is generated by using 89 kHz sine wave with 25 mV to current driver of the laser for fast frequency modulation during slow scanning of piezo driver using 1 Hz, 500 mV triangular signal. The SAS signal now detected on photodiode is passed through the lock-in amplifier to get the error signal as shown in the inset of Fig. 5.6(a). The slope of this error signal is maximized by optimizing the phase difference between reference signal and modulated signal in the lock-in amplifier; this in turn enhances the tightness of the locking point. The PID controller then takes in the error signal and generates a feedback signal which goes to the piezo driver and current controller to further stabilize the frequency at peak of the chosen SAS spectra. The relevant electronics for stabilization of reference laser with respect to Rb SAS is already discussed in detail in Chapter 4 and shown in Fig. 5.4. In Fig. 5.6(a), the process of locking reference laser is demonstrated where 780 nm laser is locked to one of the hyperfine transitions: $F = 2 \rightarrow F' = 3$ of ⁸⁵Rb. Once the laser gets locked, the scan signal which is responsible for tuning the laser frequency automatically stops and PID gets active to stabilize laser frequency and prevent it from environmental disturbance. As shown in the inset of Fig. 5.6(a), the frequency fluctuation data is recorded using highfinesse WS-7 wavelength-meter which is calibrated to its internal neon lamp reference. Tuning the parameters of electronics module decreases these fluctuations and increases the locking stability. The stabilized laser frequency with respect to Rb atom hyperfine transition is then used as reference frequency and the laser is called reference laser in our transfer cavity locking system.

In our transfer cavity system we use 780 nm laser as reference laser and 739 nm as slave laser, where our aim is to stabilize 739 nm laser with respect to 780 nm laser. In this process we first couple the reference and slave laser to a common FP cavity and detect their transmitted modes during scanning of the FP cavity length using 20 V and 1 Hz triangular wave. The optical setup for detecting transmitted modes is shown in Fig. 5.3. The FP cavity used here for transfer cavity locking technique is made of aluminium and uses a pair of confocal mirrors for generating transmitted/ reflected modes. One of the mirrors is put on an adjustable platform for manual tuning of the cavity length whereas for fine tuning in its length, a piezo crystal is attached to the other mirror. As per the voltage biasing on the piezo crystal, the attached mirror's position can be tuned from a few nm to a few μ m. As per the specification sheet, the FP cavity's FSR is approximately 1 GHz @ 805 nm with 8V/FSR and finesse > 400. The transmitted modes generated are plotted in Fig. 5.6(b), which gives the measurement of FSR and finesse of the cavity for these wavelengths. The measurement of these parameters are done by comparing data between wavelength meter and oscilloscope. When laser frequency is scanned, the wavelength meter shows the drift of wavelength and oscilloscope shows the transmitted modes; superposition of these data gives frequency interval after which transmitted modes are



Fig. 5.8: The mode of the slave laser (green) follows that of the reference laser (red) on scanning the reference laser frequency.

generated which gives the FSR of the cavity. The measured FSR of the cavity for 780 nm and 739 nm are 0.985(8) GHz and 1.007(8) GHz, respectively. Using this measurement, we also calculate the FWHM of the transmitted mode to be 4.8(5) MHz and 5.5(5) MHz and the finesse as 205(23) and 183(18) for the reference and slave lasers, respectively. The measured parameters of the FP cavity are tabulated in Tab. 5.1.

When reference laser is locked with respect to D2 line of Rb atom hyperfine transition, it is used to stabilize the optical length of the FP cavity. Peak of the transmitted mode of the reference laser formed in the cavity is used as reference point of locking. Locking the length of cavity requires error signal of the transmitted mode which requires high frequency modulation at the time of slow scanning of the length using piezo driver of the cavity. The high frequency modulation already present in reference laser for generating error signal corresponding to SAS spectrum may be used as an option but the symmetry of the error signal depends on modulation depth. As both the spectra have different FWHM, different modulation depths are required for generation of corresponding error signals. Due to this reason, we use an additional AOM in double pass configuration for frequency modulation of part of the reference laser which couples into the FP cavity. For driving the AOM, frequency modulated 200 MHz RF signal is generated by adding 96 kHz and 50 mV sine wave to the voltage controlled oscillator (VCO) control pin of the AOM driver. When reference light is passed through the AOM, it generates frequency modulated 1st order diffracted signal which is coupled to the cavity. The transmitted mode signal detected on photodiode is passed through the lock-in amplifier to get the error signal.



Fig. 5.9: Power spectral density of error signal in locked condition for SAS signal (red), error signals obtained from PDH lock of the FP cavity transmitted spectra for the reference (magenta), slave laser (green) and the DAC intrinsic noise (grey) for (a) top of the fringe and (b) side of the fringe

Generation of error signal for slave laser is also done by using the same technique, the optical setup for using AOM and coupling light frequency to FP cavity is shown in Fig. 5.3. The generated error signals of SAS, transmitted mode of reference and slave lasers are shown in Fig. 5.7(a). It is clear from this figure that the slope of error signal near zero crossing is small for SAS and large for FP cavity, which is equal to 0.018(2) V/MHz for SAS spectra and 0.091(2) V/MHz and 0.101(2) V/MHz for transmitted modes for reference and slave laser, respectively. The slope of the error signal is maximized by optimizing the change in phase difference between the modulated signal detected and the reference frequency inside the lock-in amplifier. The linear region of the error signal is also used for side of the fringe locking which additionally gives a small tunability in our system. For side of the fringe locking, we change the offset of

the error signal which causes change in zero point position of the signal thus causing change in setpoint of the servo controller. The change in offset for the error signal used for side of the fringe locking is shown is Fig. 5.7(b). In this figure, the offset of error signal for SAS has not changed because SAS spectra depends on the MB distribution of atomic gas inside the vapor cell, so the intensity fluctuation of the spectra depends on the surrounding temperature and magnetic field fluctuations around the vapor cell, which makes side of the fringe locking very unstable.



Fig. 5.10: Tunability of slave laser obtained by scanning part of the reference laser frequency that is going to the FP cavity by an AOM (green) and hence tuning the slave laser frequency using side of the fringe locking (cyan) when reference laser is locked at top of the peak of SAS signal (red). By this technique, slave laser's frequency follow scanning od the reference laser's frequency in locked condition.

Error signal generation is necessary for stabilizing the system as its slope and bandwidth of the servo controller decides tightness of the locking. Bandwidth of the servo controller depends upon the cutoff frequency of the low pass filter inside the lock-in amplifier. When the error signals for both master and slave laser are generated, we connect the error signal corresponding to reference laser to PID controller which generates the feedback signal and feeds it to the piezo driver of the FP cavity to lock its optical length. The fluctuations in the transmitted mode signal detected on the photodiode during the stabilized condition of FP cavity, are minimized using fine tuning of PID parameters and gain of error signal. Once the cavity is stabilized with respect to reference laser, the slave laser is scanned using 1 Hz, 200 mV triangular wave to slave laser piezo driver. The offset of the piezo driver voltage is tuned to search for transmitted mode in the locked FP cavity and then the corresponding error signal is generated. The error signal of slave laser is then sent to the PID for feedback signal generation and locking of the slave laser. When reference laser, FP cavity and slave laser are locked, we scan the reference laser frequency using its AOM and observe that the slave laser follows the reference laser frequency thus confirming the transfer cavity locking technique, as shown in Fig. 5.8. The stability of slave laser decreases with increasing tuning of the reference laser and infact gets locked at higher speed. When all lasers and FP cavity are stabilized, then to measure the stability of the laser frequency and length of the cavity, we take Fast fourier transform (FFT) of the locked error signal. The FFT of the signal is obtained from the Rohde & Schwarz spectrum analyzer (FSV 40), which gives the power spectral density of the noise present in stabilized system as shown in Fig. 5.9(a). Analysis of this figure shows that the least noise density is present in reference laser but when the FP cavity is stabilized with respect to it, the noise power increases. This increase is due to the intrinsic noise of the cavity which comes from temperature fluctuation, vibration and other environmental factors. On locking of slave laser with respect to FP cavity, there is a very small increment in noise power which shows that the intrinsic noise of the slave laser is very small. Using rectangular noise spectrum approximation, we approximate the linewidth of the stabilized reference and slave laser. For this, we integrate the area under the curve of the noise spectra by using numerical analysis and find the V_{rms} of the signal which are 0.0062(2) V and 0.07(3) V for reference and slave laser, respectively. Using the formula for rectangular noise spectrum analysis from the Ref. [126], we calculate the approximated linewidths for the reference and slave lasers to be 0.4(1) MHz and 1.1(1) MHz with an interrogation time of 200 ms.

In transfer cavity locking technique, there is always a finite possibility that the locked slave frequency is not at the desired frequency. Frequency tunability of the system in such cases is therefore a solution. The tunability in controlled manner is done via optical modulators like AOM and EOM. In our optical setup, the AOM used for generating error signal is also used for tuning purpose. The change in RF frequency of AOM gives a frequency offset in reference laser which causes shift in transmitted mode. When the transmitted mode is shifted, the cavity follows the mode and changes its optical length which causes a shift in transmitted mode of the slave laser too. Its corresponding servo controller then follows the shift position of the slave laser mode and tunes its frequency to follow the master laser. Thus, frequency tuning of the slave laser can be done. In our experiment, we also use side of the fringe locking technique for tuning the slave laser frequency. The error signal generated for side of the fringe locking is shown in Fig. 5.7(b). Adding a DC offset to the error signal changes the setpoint of servo controller and so the PID locks the laser frequency at side of the fringe of the transmitted mode. We characterize side of the fringe locking using the power spectral density of noise generated in stabilized error signal as shown in Fig. 5.9(b). The noise analysis shows that the noise power of slave laser during top of the fringe and side of the fringe locking are same and gives same



Fig. 5.11: Increase in tunability range of the slave laser (green) by side of the fringe locking of reference laser (red).

value of laser linewidth, this owes to the negligible intensity fluctuation of the diode laser. We also characterize and compare the tunability of our system obtained using AOM to that obtained using side of the fringe locking, which is shown in Fig. 5.10. In this process we lock the reference laser and cavity at top of the fringe and lock the transmitted mode of the slave laser at side of the fringe. During side of the fringe locking, we constantly change the setpoint of the servo controller which changes the slave laser frequency. The change in slave laser frequency is compared for both the cases. We observe that the linearity in tuning with AOM is larger than the side of fringe locking because when we change setpoint of the servo controller, the point drifts from linear region to curve region of the error signal which decreases the linearity range of the locking. To increase the linearity range of the side of the fringe locking, we use cascading technique. During the locked condition of the overall system *i.e.* both reference and slave lasers being locked by side of the fringe technique, cascading their individual tunability range results in increased tunability range of the slave laser. The possibilities of cascading and increasing the tunability range is shown in Fig. 5.11. In this figure, both reference and slave laser transmitted modes are locked at side of the fringe locking technique and the setpoint of the servo controller is changed continuously to drift the slave laser frequency. Such tunability is applied in atomic spectroscopy experiments where simultaneous operation of multiple laser wavelengths demand different detuning of these lasers at different stages, such as atom capturing and laser cooling, magneto optical trapping, state preparation by optical pumping, detection etc. These stages follow each other and require detuning of the lasers to be of the order of natural linewidth of the corresponding atomic transitions, which is typically 5-40 MHz for allowed transitions.



Fig. 5.12: Frequency drift of reference laser (Red) and Slave laser (Green)

Additional systems are thus required to set individual laser frequencies which makes the entire optical setup extensive and there are many free parameters that need to be adjusted during the operation. Here, using our transfer cavity locking setup, we show a way to simplify the system where detuning of a particular slave laser can be changed by shifting reference point of the side of the fringe locking for 5 MHz range or collectively frequency of all lasers an be shifted by tuning lock point of the reference laser, that gives wide range frequency tuning for 50 MHz range. Locking side of the fringe of the reference spectra is done by changing the offset of the error signal generated using Pound Drever Hall technique. The tunability range in this case depends on FWHM of the reference spectra used for locking.

During locking of reference and slave laser, there is always a small time dependent frequency drift observed in the experiment as shown in Fig. 5.12. The drift mainly decreases the long term stability of the locking system. The drifts arises due to change in frequency of locked Rb SAS spectrum and the air current present inside the cavity. The change in Rb SAS spectra frequency mainly depends upon the environmental perturbation on the Rb vapour cell and optical alignment between pump and probe light beam. Also the frequency difference between reference and slave laser is large which causes change in transmitted mode position due to change in refractive index arising from air current. In this figure the drift of slave laser (0.2(5) MHz/hr) follows that of the reference 0.3(5). To decrease the drift in locking system, we have to put the Rb vapour cell and FP cavity inside a vacuum chamber to avoid environmental perturbation. The stability and linewidth of reference and slave laser is tabulated in Tab. 5.2.
Line width	Stability
(MHz)	(MHz/hr)
19(1)	
18	
0.4(1)	0.2(5)
1.1(1)	0.3(5)
2.2(1)	0.6(5)
	Line width (MHz) 19(1) 18 0.4(1) 1.1(1) 2.2(1)

Tab. 5.2: Measurement of linewidth of lasers after locking in transfer cavity system

5.6. Conclusion

Many experiments require stabilized laser frequency for different purposes. Conventionally, for stabilization of laser frequency, it is locked with respect to any atomic spectra in SAS but there are several cases where no atomic transition spectra corresponding to a particular frequency is available in the SAS profile with respect to which the frequency may be locked. In this chapter, we therefore describe the transfer cavity locking technique for stabilizing such frequencies. This technique is one of the low cost alternatives for stabilizing laser frequency with respect to a reference point. In this technique, we use a laser frequency whose atomic SAS spectra is available and lock its frequency with respect to the relevant transition peak of the SAS profile. The locked reference laser frequency is used as a reference frequency and coupled into an FP cavity. The transmitted mode of the reference laser generated outside the cavity is used as a reference spectra which stabilizes the cavity optical length with respect to the mode. When length is stabilized, the slave laser frequency is coupled to the cavity and locked with respect to the its transmitted mode. In this technique, the stability of reference laser is transferred to the slave laser using FP cavity. Using this technique we are able to stabilize multiple lasers using single cavity, which reduces the cost and complexity of the experiment. The electronics which are used to stabilize the laser frequency in this technique are programmed in a single FPGA chip. In this chapter, we show the transfer cavity lock for stabilizing 739 nm laser by using a 780 nm laser which is stabilized by Rb SAS transition peak. We also show that tunability of the system can be achieved both by using side of the fringe locking technique and by tuning through AOM upto a range of 5 MHz and 25 MHz, respectively. In this technique, we find the short term line width (200 ms) of reference laser is 0.4(1) MHz and slave laser is 1.1(1) MHz with a drift of 0.2(5) and 0.3(5) MHz/hr, respectively.

Chapter 6

Computer Controlled Radio Frequency Drivers for Acousto and Electro Optical Modulators (AOM & EOM)

6.1. Introduction

All laser based experiments, *e.g.*, atomic spectroscopy, laser cooling, magneto-optical trapping, optical lattice, optical tweezers and so on require control of properties of light such as, intensity, frequency, polarization and phase [20]. For that purpose, different types of optical modulators are used that are mostly available commercially [127]. In this chapter, we discuss only about the modulators which are able to modulate frequency of laser light, *i.e.* acousto optic modulator (AOM) [128] and electro optic modulator (EOM) [129]. In these modulators, a radio frequency (RF) is coupled with the optical frequency of the laser light to produce shifted frequencies. In AOM and EOM, the efficiency of modulation or modulation depth depends on its RF frequency and power [128, 129]. In this chapter we describe detailed design of our indigenously developed RF driver to operate AOMs and EOMs. These drivers are computer controlled to automate the entire experiment.

Relevant energy level diagram for laser cooling and probing the clock transition is described in Chapter 1. As shown in Fig. 1.3, we require few hundred MHz to few GHz frequency shifted optical frequencies to drive the hyperfine transitions [20]. For that purpose we use EOMs which have higher reach in RF domain, however in that case, all modulated sidebands at lower and higher frequency relative to the principle are co-propagating [130]. Upon generation of the desired frequencies, they require time tuning between few MHz to few 100 MHz range to identify their desired values. For that purpose, we use AOMs, that diffract the higher and lower order frequencies at different angles. Thus the spatially separated different laser frequencies are easy to isolate [131]. Additionally AOMs are also used for fast switching on-off of the laser lights [132]. Thus we require to operate the AOMs and EOMs in a controlled manner and their driv-



Fig. 6.1: Schematic of frequency modulation using (a) AOM and (b) EOM. In case of AOMs, higher and lower order diffracted patterns are spatially separated, whereas in an EOM they are not diffracted but all are co-propagating [133, 134].

ing parameters are dynamic during different stages of an experiment. To drive the AOMs and EOMs remotely, we have designed and developed in-house the respective RF drivers. These drivers are required to be computer controlled, tunable and low noise because the efficiency of cooling, optical pumping and shelving depends on precise control of laser frequencies and their shot-to-shot redundancy. In this chapter, we discuss about the circuit diagram and working principle of AOM and EOM drivers which are able to generate RF frequency in a very controlled manner. We also characterize our oscillators and filters, which are used for suppressing harmonics and producing stable RF source.

6.2. Theory

When a laser beam is passed through an AOM, the transmitted light gets diffracted. In AOM, there is a crystal which is biased with a RF source by an acceptable range of frequency and power. When light wave transmits into the crystal, it gets reflected from the driving acoustic waves and interfere constructively and form diffracted beam just like Bragg diffraction seen in XRD [131]. In this diffraction process, the light is reflected from a plane which is in motion with respect to lab frame, so the obtained diffracted light is also Doppler shifted and gets frequency modulated due to photon and phonon interaction. The diffracted photons with + & - n^{th} orders of photon's frequency (n = 0, 1, 2 ...) have homogeneous angular dispersion, which depends on frequency of the RF, which is shown in Fig. 6.1(a). Modulation efficiency of AOM is measured by intensity ratio of first and zeroth order of the diffracted beam which is given as [135],

$$\eta = \frac{I_1}{I_0} = \frac{\pi^2}{2\lambda^2} \left(\frac{n^6 p^2}{\rho v^3}\right) \frac{L}{H} P_a,\tag{6.1}$$

where I_1 and I_0 is intensity of the diffracted and incident light respectively. λ , L, H, P_a , n, p, ρ and V are the wavelength of light, length and breadth of Acousto optic (AO) crystal, power



Fig. 6.2: (a) Block diagram of AOM RF driver circuit used to modulate light frequency and intensity.(b) AOM's RF driver module use in experiment

of RF used in AOM, refractive index of crystal, photoelastic coefficient, density of AO crystal and acousto velocity inside the crystal, respectively. The quality factor of AOM is [135]

$$Q = 2\pi \frac{\lambda L f^2}{nV^2},\tag{6.2}$$

where f is frequency of RF used in modulation. The quality factor describes the efficiency of frequency modulation of the AOM.

In an EOM, when an electromagnetic signal $Ae^{i\Omega t}$, that is a laser light passes through, a time dependent phase term is added in the light and side bands are generated. This is depicted in Fig. 6.1(b). The respective output can be written as [136],

$$Ae^{i\omega t + i\beta\sin(\Omega t)} = A\left(e^{i\omega t} + \frac{\beta}{2}e^{i(\omega+\Omega)t} - \frac{\beta}{2}e^{i(\omega-\Omega)t}\right)$$
(6.3)

Here, A and ω are amplitude and frequency of laser light, β and Ω are modulation amplitude and frequency of RF used in EOM, respectively. In EOM, there is a non linear crystal, whose optic axis changes with respect to the potential across the crystal. The RF source is used to change this potential around crystal periodically. As a result of this, relative rotation depends on the RF.

As brief principle of operation for the AOMs and EOMs as described, their RF drivers play important role. The generation of low noise tunable RF sources is done mainly in two ways, using a Direct digital synthesizer (DDS) [137] and using a voltage controlled oscillator (VCO) [138]. In DDS technique, we use an oscillator whose frequency is fixed and stable, a digital frequency divider and a multiplier to generate different frequencies. In this technique, we can achieve a very stable RF, the amplitude and phase of which can be controlled precisely, but the speed of tuning the frequency is slow due to the frequency divider/multiplier processing delay. In VCO based technique, there is a resonance tank circuit whose resonance frequency is changed by using controlled voltage. The controlled voltage is actually changing the effective time constant of the circuit, which changes the resonance frequency. These type of oscillators where the RF power depends on the RF frequency, are easy to use, provides a high speed of tunability but not a good frequency stability. Among these two techniques, DDS is costlier and more complex than the VCO based oscillators. We use VCO type oscillators as in our experiment, when we require rapid tuning in frequency modulation while the experiment is ongoing. To increase its frequency stability and power, we use phase locked loop (PLL) [139] with DDS and voltage variable RF attenuators (VVA), respectively.

6.3. Circuit Diagram for RF drivers

Circuit designing of RF drivers for optical modulators require VCO, VVA, reference frequency source, filters, amplifiers and automation unit. The range of frequency shifts for AOM is of the order of MHz whereas that of EOM is GHz. Due to this reason, the circuit designing for AOM and EOM RF drivers are different as their principles of operation are different.

Figure 6.2 shows the block diagram of RF driver for AOM which uses VCO, VVA, filters, amplifier and digital to analog (DAC) converters. As shown in the figure, we first generate control pin voltages for VCO and VVA separately using DAC and DACM to generate their desired output. These voltages are generated through a DAC where the digital bits for DAC comes from a digitally operated addressing and control module (DACM)(described in Chapter 3) which is connected with data acquisition (DAQ) card through a digital data bus. The DAQ card is connected to a computer where a LabVIEW based GUI aids in controlling parameters such as, frequency and RF power of the driver. The VCO generates a signal whose frequency depends on the control pin voltage of the VCO. The signal generated by VCO is stable within 0.2 MHz/h, which is good enough for our application, but contains higher order harmonics. To

S.No	Items	Quantity
1.	Mini-circuit VCO POS-300	1
2.	Attenuator Mini-circuit PAS-2+	1
3.	DAC -AD7541	2
4.	RF amplifier MAR3+	2
5.	OPAMP-OPA189	6
6.	Buffer BUF634	6
7.	Buffer MAX4204	1
8.	Decoder 74LS138	4
9.	Quad NAND 7400	2
10.	Monostable 74LS123N	1
11.	Latch 74LS373N	4
12.	LT1763	3
13.	LT1175	2
14.	47 nH air core SMD	10
15.	51 nH air core SMD	10
16.	100 nH air core SMD	10
17.	10 nH air core SMD	10
18.	15 pF ceramic capacitor	10
19.	50 pF ceramic capacitor	10
20.	100 nF ceramic capacitor	50
21.	10 nF ceramic capacitor	50
22.	22 pF ceramic capacitor	10
23.	30 pF ceramic capacitor	10
24.	$10 \ \mu F$ polar capacitor	4
25.	10 nF ceramic capacitor	10
26.	$1 \ \mu F$ polar capacitor	4
27.	XLR male PCB mount connector	1
28.	DB25 Male Connector	1
29.	DB25 Female Connector	2
30.	1k SMD resistance	16
31.	150k SMD resistance	6
32.	47k SMD resistance	2
33.	10k SMD resistance	8
34.	12k SMD resistance	4
35.	3.2k SMD resistance	4
36.	442k SMD resistance	2
37.	SMA Female PCB mount	1
38.	LED	1
39.	Jumper	4
40.	2U Box	1

Tab. 6.1: List of items required for fabricating the RF driver for AOM following the schematic as shown in Fig. 6.3(a)



Fig. 6.3: (a) Schematic and (b) populated printed circuit board of an RF driver for driving AOMs.

suppress the harmonics, we use LC type low pass filters and the output of filter is connected to the VVA. The VVA attenuation depends upon the input frequency and its control pin voltage. Thus, using VVA, the RF power of the harmonics generated in the output frequency of the VCO can be controlled and suppressed by 40 dB than the principle frequency. The output signal from VVA is then connected to an [!ht] amplifier and used in AOM modulator for frequency and intensity modulation. The schematic of developed AOM driver is shown in Fig. 6.3(a), where the designed RF source is able to generate 150 MHz to 290 MHz, with RF power variation from -10 dBm to -70 dBm. The step size of the output frequency can be controlled to 0.02 MHz and its output power with a step size of 0.3 dB. In designing of this circuit, we use an adder circuits before the control pins of VCO and VVA, where we add voltage from DAC



Fig. 6.4: (a) Block diagram of EOM RF driver and (b) the developed hardware mounted in a 19 inch rackmount 2U box

and voltage from external source. The purpose of this adder circuit is to control parameters using external source or to stabilize frequency/power using external servo controller. The use of external source is an optional connection in our RF driver which helps for providing extra stability thus making it a user friendly device. In the board we mounted the VCO and the LC type 4^{th} order filter in a PCB mount grounded case for electromagnetic shielding, which is shown in the Fig. 6.3(b). In PCB design of AOM driver, we take care about the impendence matching and the grounding shielding of the RF signal which suppress high frequency pickup noise and help to improve signal quality. Two address module PCBs for controlling VCO and VVA together with the described AOM driver PCB are mounted on standard 19 inch 2U box.

Circuitry of the EOM driver is more complex than the AOM driver, since stabilization at high frequency is not trivial. The block diagram for the circuit of EOM driver is shown in Fig. 6.4. In this, the RF frequency shift is in a range of few GHz which requires extra care for the



Fig. 6.5: (a) Schematic and (b) populated PCB board for the EOM RF driver

electronics components during the designing of PCB. The circuit for RF driver of EOM has three parts: a DDS board used to generate reference frequency, VCO for generating required frequency and phase locked loop (PLL) to stabilize the VCO output frequency. The PLL compares the reference signal and the output signal from VCO and generates a feedback voltage which is connected to the control pin of the VCO and stabilizes the output frequency of the driver. The schematic and PCB for EOM driver are shown in Fig 6.5(a). The developed hardware is able to generate 1.5 GHz - 3.5 GHz which is tunable with a step size of 10 MHz.

Our DDS circuit is able to generate 20 MHz frequency which is very small compared to the output frequency of VCO, due to this reason we use frequency divider, which divides VCO

S.No	Items	Quantity
1.	AD9912A DDS evaluation board	1
2.	Mini-circuit ROS 3800	1
3.	Coupler BDCN-14-342+	2
4.	Frequency divider HMC363	3
5.	Phas/frequency discriminator AD9901	1
6.	Buffer BUF634	2
7.	Buffer MAX4204	3
8.	Opamp MAX4304	2
9.	OP07	6
10.	LT1175	6
11.	LT1763	6
12.	Timer 555	1
13.	Quad NAND 7400	1
14.	47 nH air core SMD	10
15.	51 nH air core SMD	10
16.	100 nH air core SMD	10
17.	10 nH air core SMD	10
18.	15 pF ceramic capacitor	10
19.	50 pF ceramic capacitor	10
20.	100 nF ceramic capacitor	50
21.	10 nF ceramic capacitor	50
22.	22 pF ceramic capacitor	15
23.	30 pF ceramic capacitor	20
24.	$10 \ \mu F$ polar capacitor	10
25.	10 nF ceramic capacitor	50
26.	$1 \ \mu F$ polar capacitor	10
27.	XLR male PCB mount connector	1
28.	DB25 Male Connector	1
29.	DB25 Female Connector	2
30.	1k SMD resistance	25
31.	150k SMD resistance	8
32.	47k SMD resistance	10
33.	10k SMD resistance	12
34.	12k SMD resistance	8
35.	3.2k SMD resistance	10
36.	442k SMD resistance	8
37.	SMA Female PCB mount	3
38.	LED	1
39.	Jumper	4
40.	2U Box	1
41	Toggle switch	1

Tab. 6.2: List of items required for designing RF driver for driving the EOMs $\,$

output 256 times. Then the output frequency of divider circuit is in range of DDS frequency, which is now easily comparable in PLL and the output of PLL is fed back to the control pin of the VCO. In this process, VCO frequency is locked with respect to the DDS frequency and DDS master clock is locked with respect to a reference clock from an atomic clock source. Once VCO is locked with respect to DDS, RF frequency tuning can be done only by changing DDS output frequency. The VCO follows the DDS and generates output whose frequency is 256 times the frequency of DDS. In designing of PCB for EOM driver, we place all components as close as possible in the PCB, so that no interaction or antennal effect occurs in between IC to IC. As visible in Fig. 6.5(b), some parts are left unpopulated as a future option for implementing auto scan of EOM's VCO during large range of tuning and frequency multipliers which are used in different EOMs requiring high frequency in our trapped Yb ion experiment during laser cooling.

6.4. Result and Discussion

In our experiment, we have used two types of AOMs whose operating ranges are between 150 -250 MHz and 50 - 100 MHz with central frequency 200 MHz and 80 MHz respectively. In this section, we show results of the RF driver which is able to generate 150 MHz to 250 MHz. We have used Mini Circuit POS-300 as the VCO and for VVA, we use Mini Circuit PAS-2+. The VVA that we used are actually a current control attenuator that works under the principle of current to voltage convertor. Figure 6.6(a) shows tunability of the RF driver with respect to the voltage which is applied to the control pin of the VCO. We control this voltage using a 12 bit DAC whose voltage resolution is 1.0(1) mV and stability is 0.4(1) mV over a period of 24 hour operation. Using this DAC, we are able to tune from 129.16(8) MHz to 265.30(8) MHz with a resolution of 20(8) kHz. The voltage controlling frequency of the VCO is (9.8 ± 0.1) MHz/V, which is calculated by a linear fit of the data taken in the region between 130 MHz to 160 MHz, as also shown in Fig. 6.6(a). The VCO used in this circuit generates fundamental frequency and additional higher harmonics, which is not desired for the experiment in case we use a high bandwidth modulator. To overcome this problem, we use LC type low pass and high pass filter and by cascading them we create a bandpass filter whose pass range is between 130 MHz to 170 MHz. We also use VVA whose attenuation factor is frequency dependent, and which helps to suppress the harmonics or any high frequency pickup noise. Figure 6.6(b)shows the RF power control using VVA, filters and amplifiers which follow a non-linear profile saturating above certain level of the voltage. In this attenuation control, we plot for three different frequencies 150 MHz, 200 MHz and 250 MHz, so that we are able to find its linear region which is nearly universal for every frequency. In our system, we are able to control 20 dBm RF power with a rate of nearly 7.4(1) dBm/V for a voltage range 4.0(5) to 6.0(5) V with a resolution of 0.3 dBm. We use this linear region to stabilize the RF power using external servo



Fig. 6.6: (a) Tuning VCO frequency with respect to the voltage given at the control pin (blue), and a linear fit to it (red). (b) The VVA characterization with respect to the voltage given at control for different input frequencies 150 MHz (green), 200 MHz (magenta) and 250 MHz (orange).
(c) Bode plot of the filters used in RF driver, that shows power vs frequency for fundamental signal (purple) and second harmonics (pink)

controller, where we clamp our servo controller in this region for tight locking of RF power or also use for laser intensity stabilization. The filters and VVA also produce a transfer function of the form $1/\omega$, which we obtain through measurement, corresponding plots of which are shown in Fig. 6.6(c). In this plot, we show the attenuation of second order filter which confirms about the filter response and purity of the signal. The RF power in second harmonic is suppressed by 40 dB relative to the fundamental which is due to dual stage 3^{rd} order low pass filter. The power variation of fundamental frequency in pass region of filter is upto 4.2(2) dBm from 150 MHz to 250 MHz due to the LC filter, which creates a bump in transfer function near its cutoff region. We also calculate the phase noise of the RF source which is -94.2 dBc/Hz @ 80kHz when signal amplitude is -17 dBm and frequency is 166.2 MHz. At 200 MHz and at -6.92 dBm, phase noise produced is 130.34 dBc/Hz @12.4 MHz. The phase noise measurement, concludes that our frequency stability is quite good as per our experimental requirement and there is no extra need of servo controller to stabilize the VCO frequency. However, in case we use the RF driver for frequency scanning upto few tens of MHz, then there is a requirement of intensity stabilizer to stabilize the RF power during tuning of frequency.

As per the requirement of experiment, the EOMs generate side bands of the fundamental frequency to drive the transitions between different hyperfine levels of the associated states. In our experiment, we require optical frequencies shifted by 2.1 GHz, 14.7 GHz, 3.07 GHz, 12.6 GHz and 10 GHz for the Yb ion spectroscopy. Since the desired RF frequencies are known, the EOM drivers are designed in a way so that they provide stable frequency and about 400 MHz tunability. Here we characterize our EOM driver which we use for generating 3.07 GHz for repumping lasers at wavelength 935 nm. In designing this driver, we use mini-circuit VCO ROS-3800, which generates 1.7 GHz to 3.9 GHz. In this circuit, we are not using any attenuator because the output intensity of laser does not depend upon the RF power. The magnitude of side bands however depend upon the RF power which we control using power amplifiers used in the EOM drivers. Stabilization of the VCO frequency is done by using a commercial AD9912A DDS board which can generate 10 Hz to 100 MHz and the master clock of this DDS board is locked to an ultrastable reference frequency at 10 MHz, which comes from Cs atomic clock or Rb clock or H-maser or GPS receiver or elsewhere. When we supply a voltage to VCO, the output signal is passed through a bi-directional coupler, where one part moves towards main output and other part passes through a frequency divider. The frequency divider output and DDS output is connected to PLL and the output is fed back to VCO which generates a stabilized frequency. The tuning of VCO frequency is completely dependent upon the tunability of DDS. In Fig. 6.7(a), we show the tunability of VCO with respect to the voltage applied to control pin of the VCO. This VCO has a linear range from 1.8 GHz - 3.5 GHz, where we find smooth voltage controlled frequency tuning of (132 ± 2) MHz/V. The voltage requirement to generate 3.07 GHz is about 9.3 V to VCO's control pin, the PLL used in our circuit is not



Fig. 6.7: (a) Tuning VCO frequency with respect to voltage given to control pin (black), also a linear to it (cyan). (b) Bode plot of the filters use in the RF driver, where we plot the power and frequency for fundamental signal (purple) and second harmonics (magenta)

able to generate this amount of voltage, so we additionally put a low noise OPAMP OP-07 which helps the PLL to reach the desired voltage. In our circuit, we use a bi-directional coupler whose operating range is 1.7 - 3.4 GHz and a band pass filter that removes any harmonics. This makes an equivalent bandpass filter, whose transfer function is measured and plotted in Fig. 6.7(b). The figure also shows the second harmonic power so that it can be easily compared with the power associated to the fundamental frequency. The required pass band from range of 2.6 - 3.1 GHz in this filter has a power variation up to 6(1) dBm and the difference between second and fundamental power is up to 40 dBm, which also clarifies about the 6^{th} order band

pass filter in this region. We also measure the phase noise at an operating frequency of 3.07 GHz is 125 dBc/Hz @ 17.70 MHz at an RF power of 10 dBm. The frequency stability of the VCO is measured to be 100(20) kHz and the power fluctuation is 0.08(3) dBm during 1 hr measurement. In our experiment, once the RF driver is fixed at a certain frequency, it does not require any further tuning.

6.5. Conclusion

In this chapter, we have described the requirement of the optical modulators *i.e* AOM and EOM along with the design specifications and working principle of the RF drivers which are used to drive these modulators for frequency modulation. The characterization parameters of one of the AOM drivers operating in the frequency tuning range of 150 - 250 MHz has also been reported. This AOM driver is completely computer controlled using our DACM modules which is previously described in Chapter 3. In AOM driver, we have used VVA which controls the RF power fluctuation and stabilizes laser intensity via an external servo controller. The EOM RF driver is able to generate 3.07 GHz frequency with a stability of 100(2) kHz and has a tunability range of 2.6 -3.1 GHz. We obtained transfer function of the filters which are used to suppress the higher order harmonics upto 40 dB during the generation of RF signal. Both EOM and AOM drivers are used in the optical setups of our Yb-ion experiment.

Chapter 7

Conclusion and Outlook

The accurate reference for time which is the most essential and fundamental unit of measurement that can be obtained with highest precision from atomic clocks. An atomic clock is nothing but precise measurement of an atomic transition frequency which is constant in nature and least perturbed by environmental factors which can be controlled. Most of the developed/developing countries maintain their local time using atomic clocks as it has direct linkage to the country's growth and security. Thus generation of accurate local time from state-of-the-art atomic clocks, maintenance of standard time and dissemination of that to the users is necessary. Together with that more accurate clocks with improved accuracy need to be developed to meet the requisites. In India, at CSIR -NPL, which is the NMI of the country, we are designing a state-of-the-art optical atomic clock using single trapped Yb ion, that will be able to generate time with an inaccuracy of 1 s over the age of the universe that is in other way few parts in 10^{18} . In future, optical atomic clock is also expected to be giving the new definition of SI second.

Developing the Yb ion based optical clock involves the accomplishment of a plethora of subsegments including designing, fabrication, instrumentation, various measurements, in the areas of vacuum system, optical setups, electronic modules and so on. This thesis expounds a wide variety of essential instrumentation that are part of the precision ion-trap experiment. These include indigenously designed and developed devices which are requisites of the experiment and laser locking techniques together with associated hardware to lock several lasers to be used and so on. An additional detailed theoretical study on estimation of BBR shift, magic wavelength and tuneout wavelength, has also been explicated in the thesis.

The protocol of the planned single trapped ion experiment includes production of Yb atoms, ionising them to produce Yb-ions, trapping, laser cooling and clock transition probing. During the experiment, the clock states get perturbed due to several external factors leading to uncertainty in the measurement, which can not be ignored to operate it as an accurate clock. Among several environmental factors, BBR from the surrounding body is one of the major contributors for perturbing the clock states. We have therefore calculated the energy shift due to BBR using theoretical and experimental data, as described in Chapter 2. The static polarizabilities for the ground state ${}^{2}S_{1/2}$ and the metastable state ${}^{2}D_{3/2}$ are calculated using which the BBR shift is estimated to be -0.44(3) Hz at 25 °C. Our estimated values have improved accuracy compared to the previous estimated values [58], which matches well with the recent experiment [59]. The calculated polarizability value also aids in obtaining the magic wavelengths, tune out wavelengths for possible optical trapping of Yb⁺ in an optical lattice. The results obtained in this study will be of immense interest for carrying out precision measurements in the $[4f^{14}6s]^{2}S_{1/2} \rightarrow [4f^{14}5d]^{2}D_{3/2}$ transition of Yb⁺ for attaining high accuracy clock frequency and investigating fundamental physics.

In our experiment, the process of ion trapping to probe the clock transition eventuates within a few milliseconds and thus requires control of many electronics module like DC voltage source, tunable RF frequency to drive the optical modulators like AOMs, EOMs, mechanical shutters for fast switching on-off of the laser beams, current and voltage controller etc. To control all these modules manually within a small interval of time within which the experiment to be carried out, is not possible and thus they require complete automation so that their sequential operations are free from any human errors. We have developed an automation unit that is a DACM module, which hooks up with our every indigenously developed modules and connects each module through a common digital data bus using serial and parallel hybrid type communication protocol. The presented DACM module in Chapter 3 is able to control 64 different modules distinguished by unique individual addresses using 8 bit for address and 16 bit for controlling them. Performance of the DACM module is tested by using our indigenously developed ultra stable programmable DC voltage source, as described in Chapter 3. In our trapped ion experiment, the machining error due to fabrication of the ion trap causes significant offset of the potential minima relative to the center of the vacuum chamber. Due to this offset, ion position gets shifted from the center and as a result of that considerable amount of systematic shift arises. To minimize such errors, we have developed a low noise ultra stable DC voltage source which is connected with the compensating electrodes near the ion trap to control the motion of the trapped ion. The voltage source is able to generate 0 - 100 V DC with a resolution of 3 mV and long term stability of 1.9 mV over a few days. This voltage source is fully computer controlled through our DACM module and programmable from a remote PC. Further it is also used as a low frequency, high amplitude arbitrary waveform generator, which is useful for biasing piezo to tune different parameters in the experiment in a very controlled manner.

Once the ion is trapped, it has to be laser cooled to sub mK temperature for accurate probing of the clock transition. The lasers to be used for laser cooling need to have a high quality factor and good frequency stability, which requires an active frequency stabilizer and a reference frequency. This necessitates the development of a laser frequency stabilization hardware module, which is able to lock the laser with respect to any atomic transition spectra or reference Fabry Perot cavity. For that purpose we have designed an developed an FPGA based module that incorporates all required modules in it. Its details are described in Chapter 4. The standard stabilization of laser frequencies requires function generator, lock-in amplifier and PID controller, which are programmed inside of a single FPGA chip. The bandwidth of our module is 100 kHz which can be extended further by different choice of ADC if required. The characterization of our FPGA based module is performed by locking a 780 nm laser with respect to D2 hyperfine transition of ⁸⁷Rb by a saturation absorption spectroscopy. In order to test universality of the instrument, we further tested its performance by using it in a Auger spectroscopy and optical coherence tomography. Compared to available commercial systems, we incorporated auto relock mechanism, that locks the laser to its set frequency automatically without any manual intervention. Using this module, we are able to lock laser frequency for more than a week in normal environmental conditions of the lab

The stability of laser frequency depends on stability of the reference points, which are typically generated by atomic transition spectra, transmission peak of ULE cavity and reference output of an optical frequency comb. In our Yb ion trap experiment, reference frequency generation via conventional SAS spectra is not possible for locking the required lasers with respect to an atomic transition. Other options e.g., ULE and frequency combs are very costly for the required purpose and also increase the experimental complexity. To encounter this problem we use another simple technique called transfer cavity locking, where we use a reference laser and first lock it's frequency with respect to an atomic transition, then we use this reference laser frequency to stabilize optical length of a FP cavity. The FP cavity is then treated as the reference cavity where we couple the lasers to be used in ion trap experiment and use PDH technique to stabilize their frequencies. The entire electronics for three stage locking, that are the reference laser, FP cavity and usable laser, are programmed inside of a single FPGA chip. This makes the system less noisy, compact and cost effective. We have demonstrated locking of the 739 nm laser using this transfer cavity locking, where the FP cavity is locked using a stabilized 780 nm laser. In this technique we are able to lock the 739 nm laser for more than two days. The details are described in Chapter 5.

The presence of hyperfine levels as shown in the energy diagram of Ytterbium, affirms the requirement of laser frequencies shifted by few hundred MHz to few GHz to drive different hyperfine transitions. Frequency of light can be shifted by using AOMs and EOMs. Chapter 6 of this thesis reports about the design and development of their respective drivers. Both the drivers are fully computer controlled. The AOM driver has frequency tuning range of 150 -

250 MHz, with a resolution of 20 kHz and suppresses the adjacent harmonics by 40 dB. The EOM, on the other hand has a tunability range of 2.6 -3.1 GHz with a stability of 100(2) kHz and resolution of 50 MHz and offers 40 dBm harmonic suppression.

Many essential building blocks as required for developing the complex experimental setup for trapped ion based optical atomic clock are developed within this thesis work. The described work brings a step forward towards realization of the planned experiment. At present, upon development of the described instrumentation, we are in a stage of Yb-ion production, its trapping in the ion trap and laser cooling. It is expected that the future works using the developed essential components within the scope of this thesis will lead CSIR-NPL towards completion of mission mode project "Quantum standard of time Via optical frequency standard".

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